

# Developing Complex MIL/Aerospace SoS at Minimum Risk and Cost

Horst Salzwedel, CTO

MLDesign Technologies

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- Aerospace systems are complex SoS that have to be designed to cope with rapidly changing architectures and challenges.
- Their main technological advancements today are driven by the progress in readily available electronics and the ability to develop disruptive systems rapidly at minimum time, cost, weight, power consumption and maximum security (before competitor).
- There are no engineers who will understand the complex interactions of such SoS without an executable model. The development can therefore only be carried out with mathematical models at mission level. The elements of models have to be physical laws or other validated facts.

The challenge is to develop methods and software for design/development that minimize the risk of development of these highly complex SoS

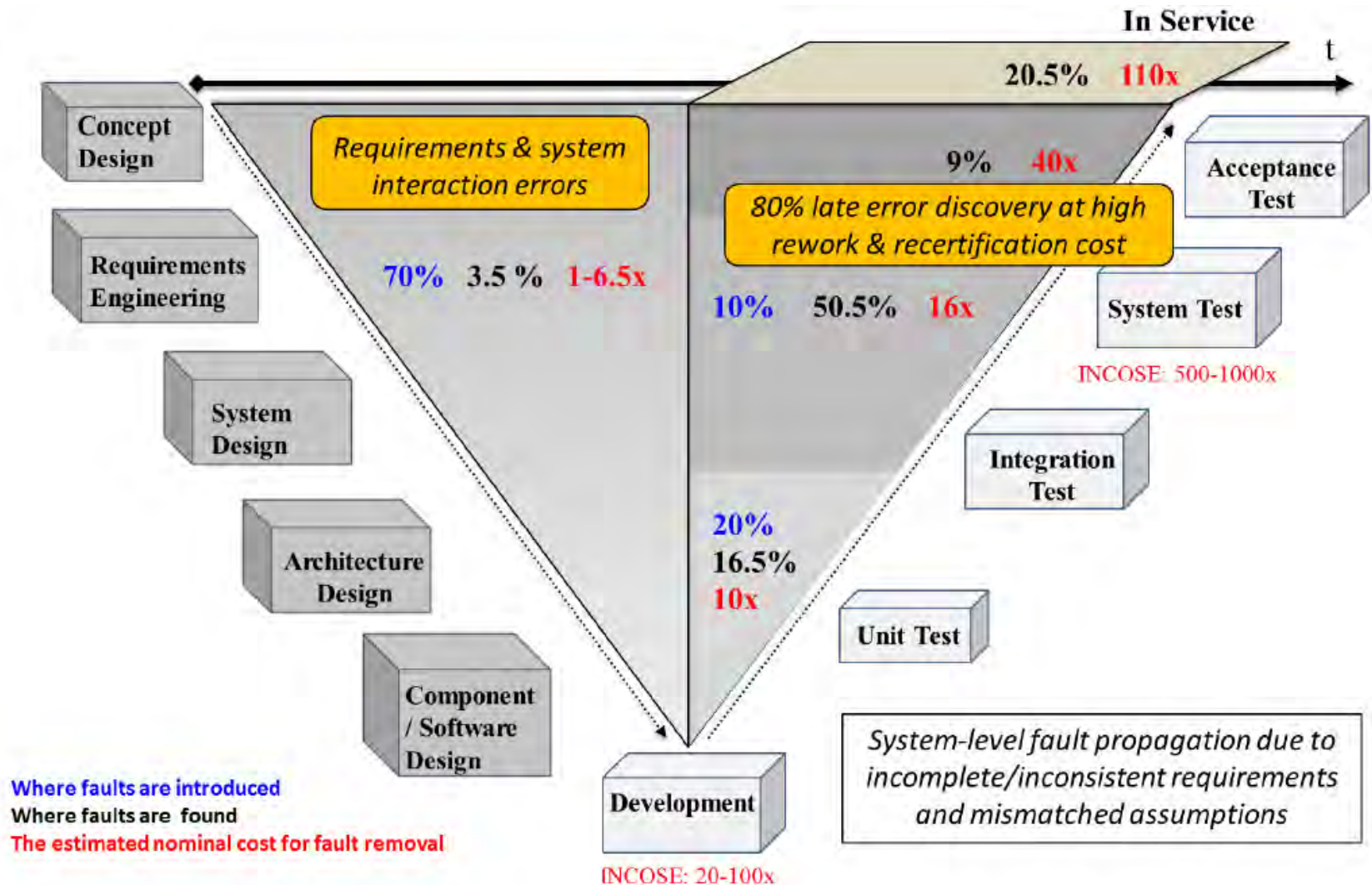
# What are complex aerospace SoS?

All aerospace vehicles are multi-disciplinary, dynamically coupled systems of systems (SoS), with dynamical interactions of,

- Vehicle
- Pilot (and/or Robotic system)
- Environment
- Propulsion system
- Avionics system
- Networked digital electronics
- Payload
- Coupled autonomous vehicles

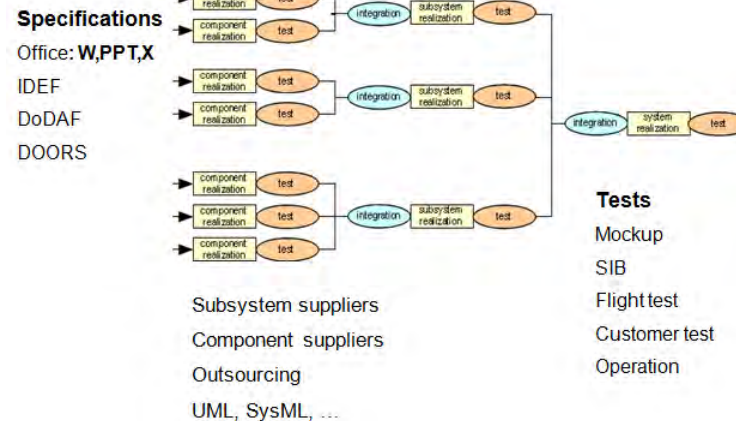
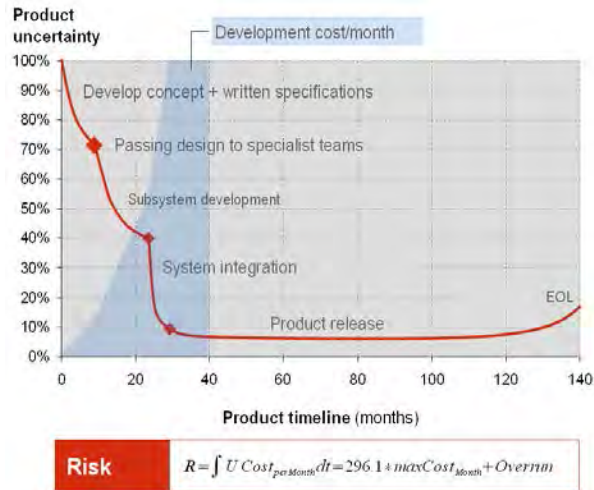


# Results of current industry design methods



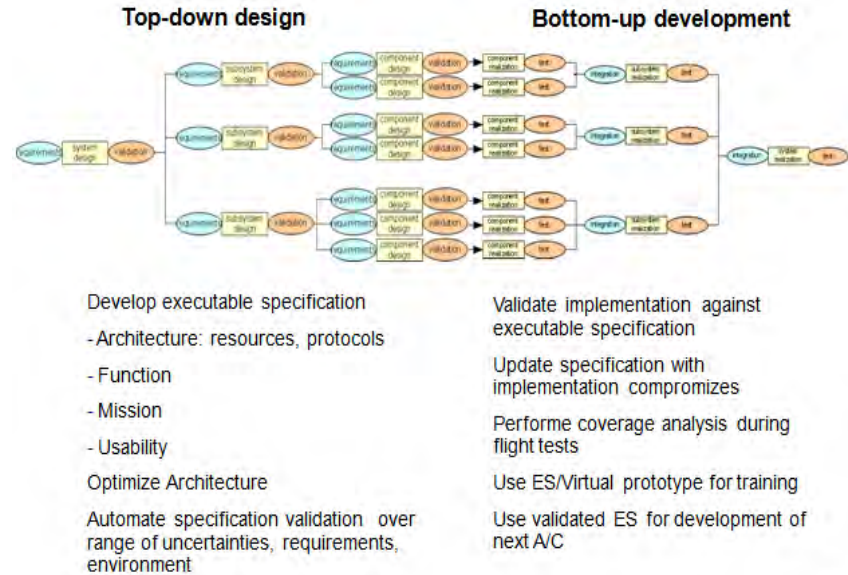
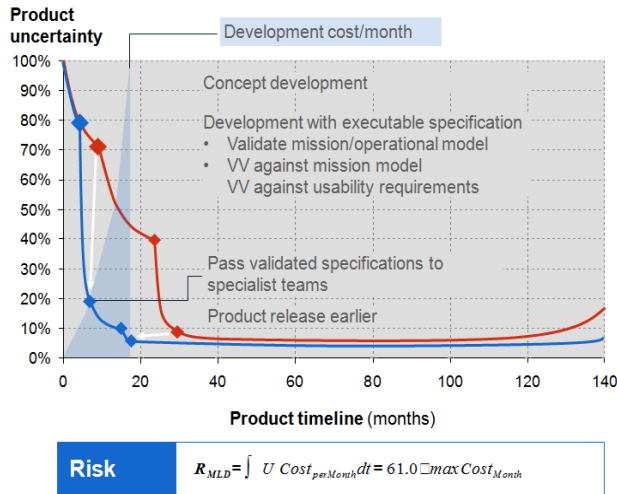
Aerospace made fantastic progress, but recently runs into cost and time overruns of >\$100B/year

# Risk of Classical Design Flow



- ➔ Classical Design Flow maximizes risk
- ➔ Cannot optimize for top level requirements
- ➔ Has lead to development time and cost overruns, project cancellations, and thousands of dead pilots in WW I
- ➔ Is unsuitable to develop disruptive technologies

# Mission Level MIL/Aerospace design flow

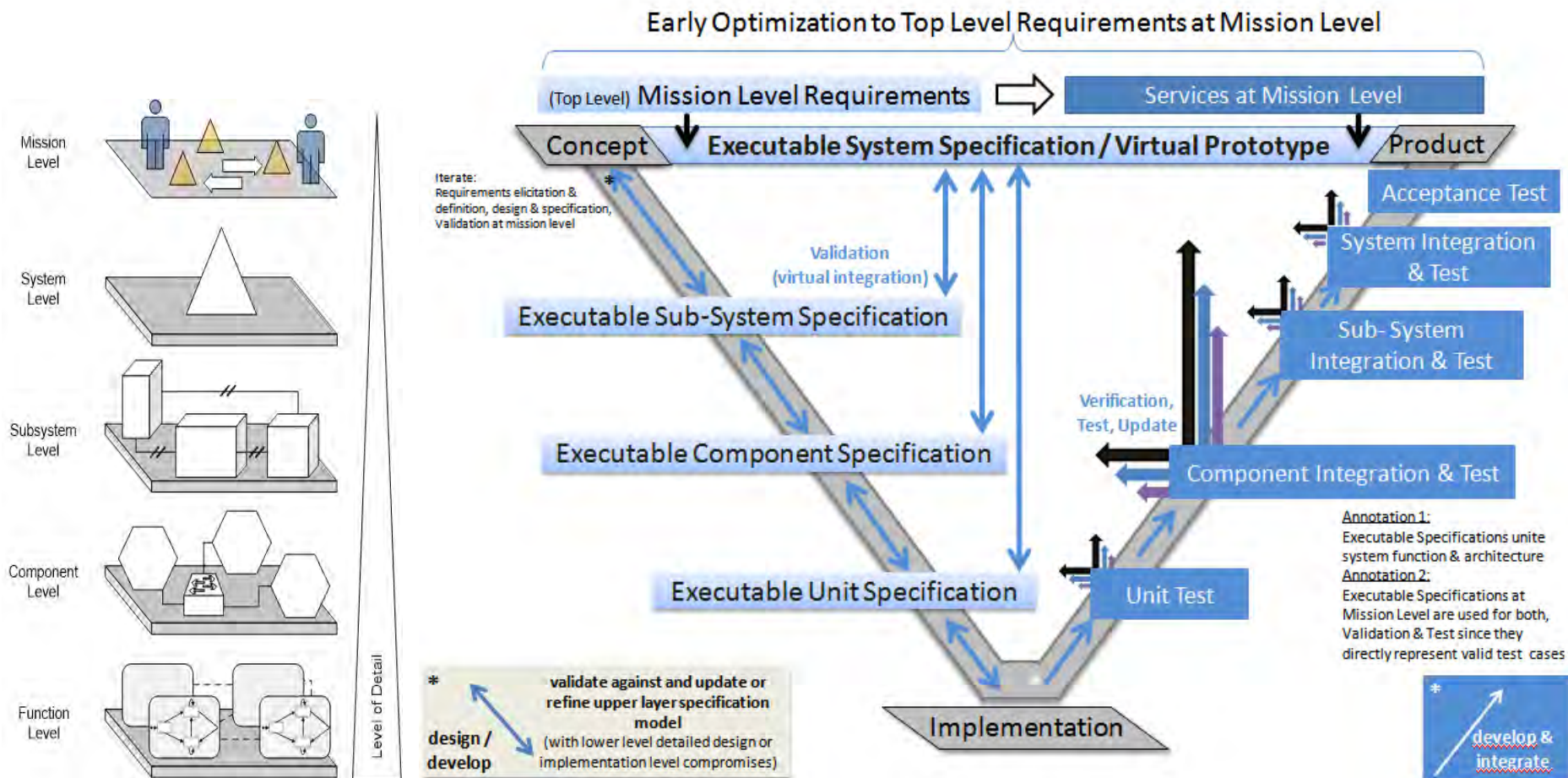


- MR-MBSE design integrates top-down design with bottom-up development
- DoD requires that developed executable specifications are updated and validated during all phases of development, including concept development, technology development, subsystem development, integration, test, training and life cycle management
- Both Boeing and Airbus use this approach



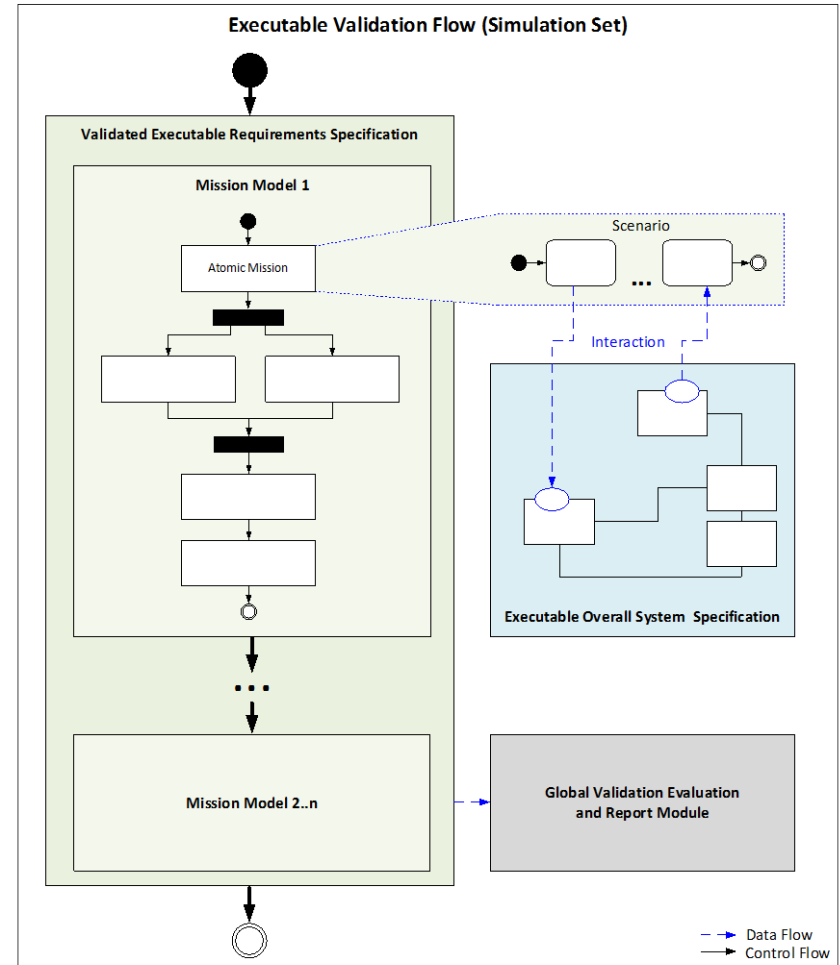
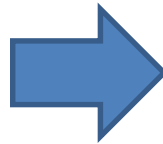
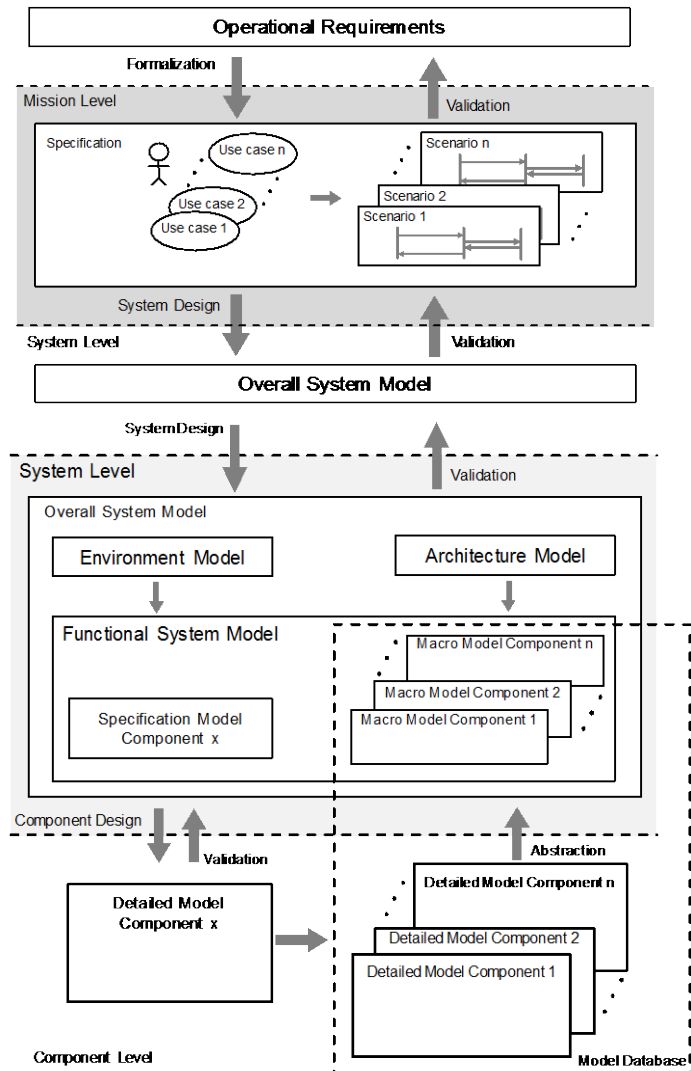
# MINIMUM RISK DESIGN

## DEVELOPMENT PROCESS



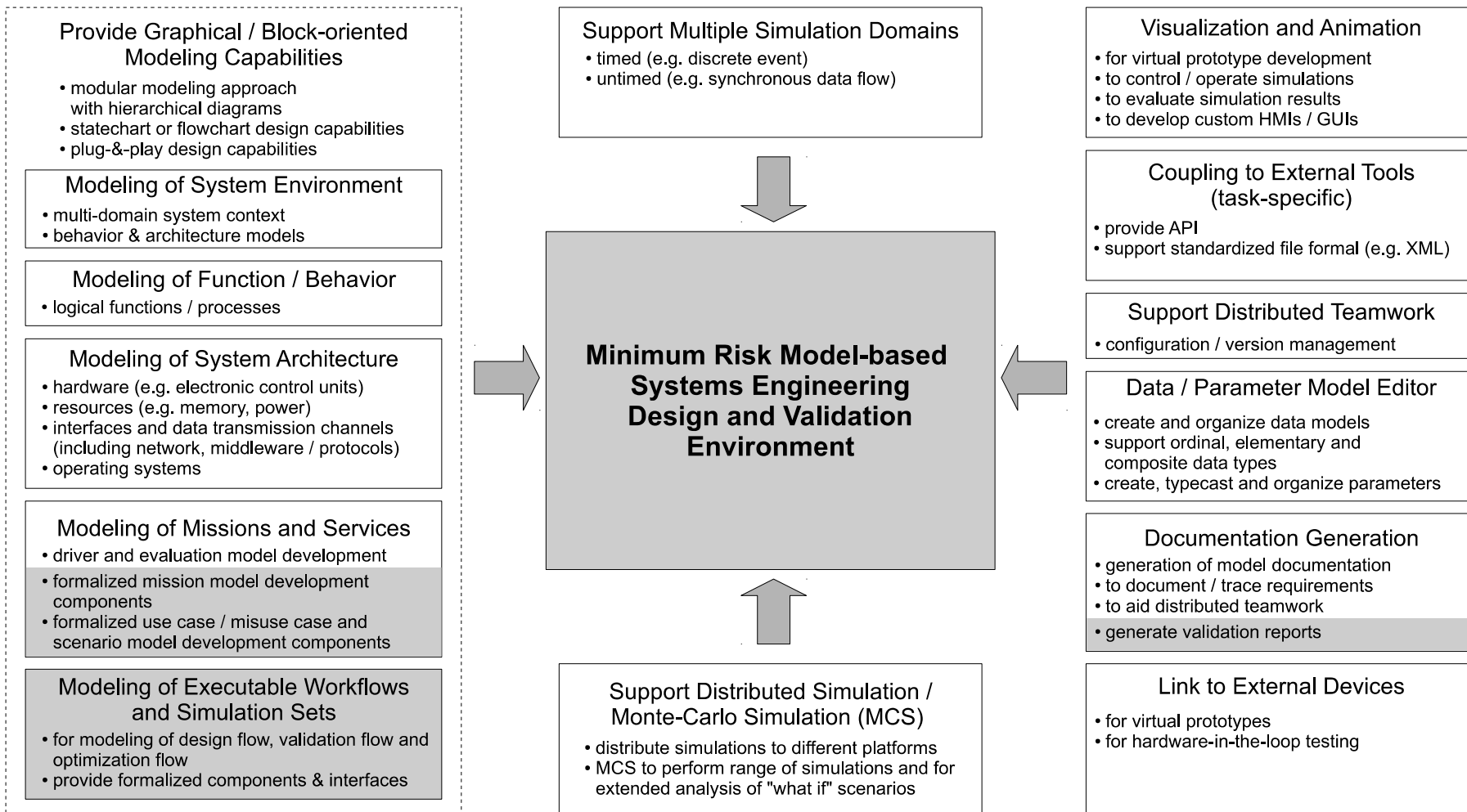
# MISSION LEVEL DESIGN WORKFLOW

## FROM REQUIREMENTS TO EXECUTABLE SPECIFICATION



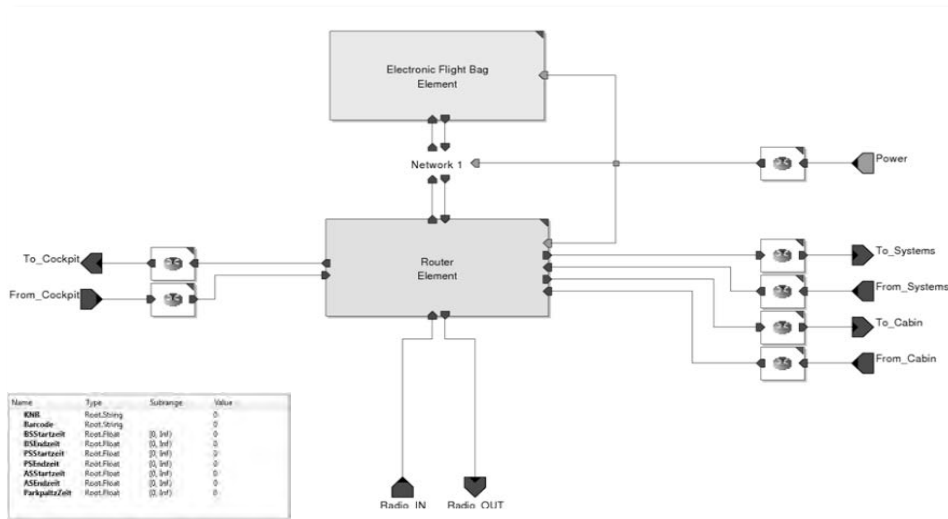


## GENERAL FEATURES



□ Fully supported by MLDesigner

■ Extension feature (to be provided)



### KEY CHARACTERISTICS

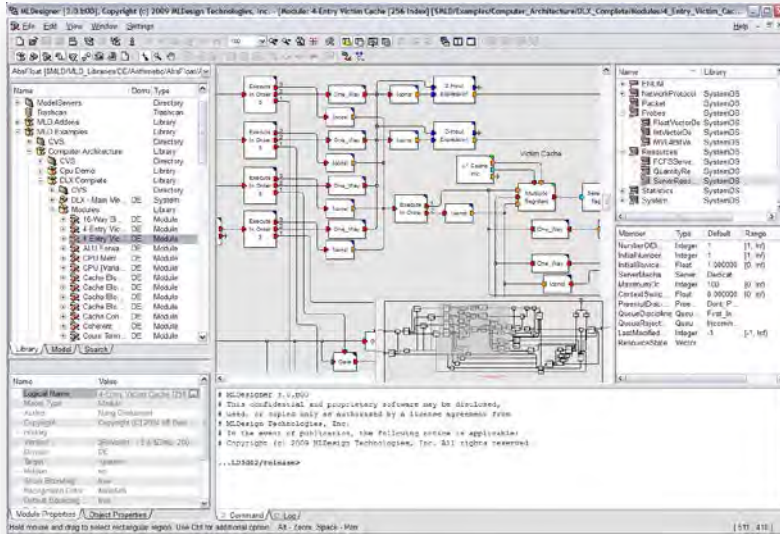
- Modeling & simulation of complex systems, systems of systems and processes at mission level (use-case and test-case driven)
- Free modeling of data & data transport systems for any purpose
- Arbitrary level of abstraction (top-down, middle-out)
- User defined extensions with means of C/C++ or any other programming language w/ C++ bindings
- Interfacing for x-in-the-loop approaches
- Interfacing to arbitrary tools via extensions (e.g.: database systems, reporting engines)
- Lightweight XML model storage e.g. for version control systems, configuration management and linking with requirement management tools

### BUSINESS BENEFITS OF MLDESIGNER

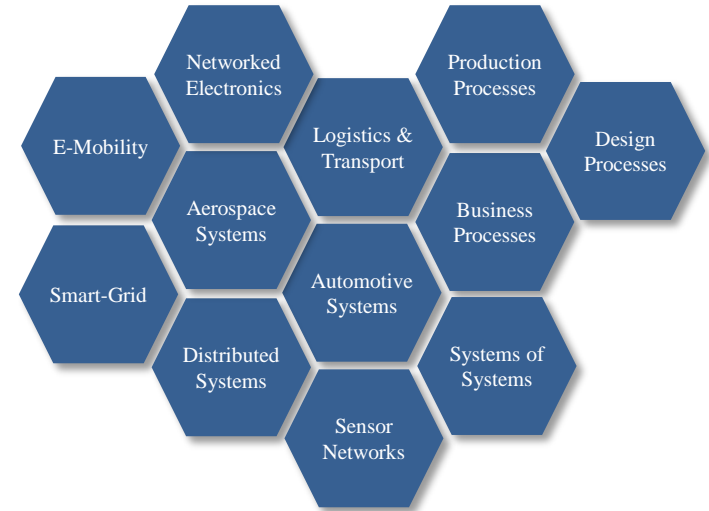
- Analyze dependencies in complex system designs
- Visualize and study complex sys.-behaviour
- Test subsystem integration during concurrent development phases (Middle-out)
- Detect, disclose and track inaccurate/faulty system requirements to its origins



# MLDesigner



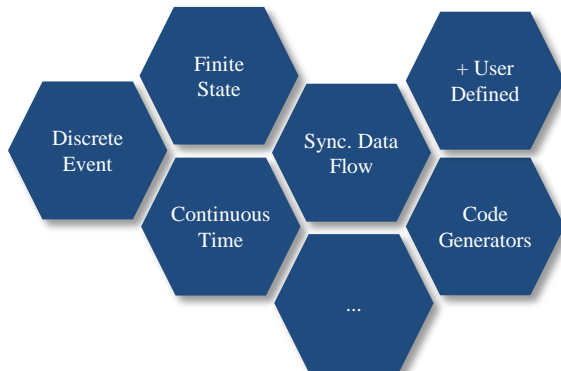
## FIELDS OF APPLICATIONS



## GENERAL BENEFITS OF MLDESIGNER

- *Reduction of project/product risk & costs by using **virtual prototypes***
- *Early **optimization** of system design*
- *Early validation of **functional** requirements*
- *Evaluation of **non-functional** requirements*
- *Prototyping of **HID** (human interface device) for early **user-acceptance tests***
- *System models & results of analyses may be used in **product certification process***

## MODELS OF COMPUTATIONS



# MRD 1: B777 development → MR-MBSE of networked systems

The design of the B777 included >200 ECUs that communicated over shared bus systems to save weight. To reduce the high risk of such a development Boeing introduced MR-MBSE of networked avionics systems.

- Performance level executable specifications + simulation with BONEs permitted
  - aircraft level analysis/validation of avionics/cabin systems
  - reliability analysis during concept development
- The B777 avionics development time was reduced 2x + achieved the highest availability of any aircraft



## MRD 2: A380 → Aircraft level specification validation of AFDX

### Development of a performance and architectural modeling and verification environment for A3XX AFDX based cabin systems

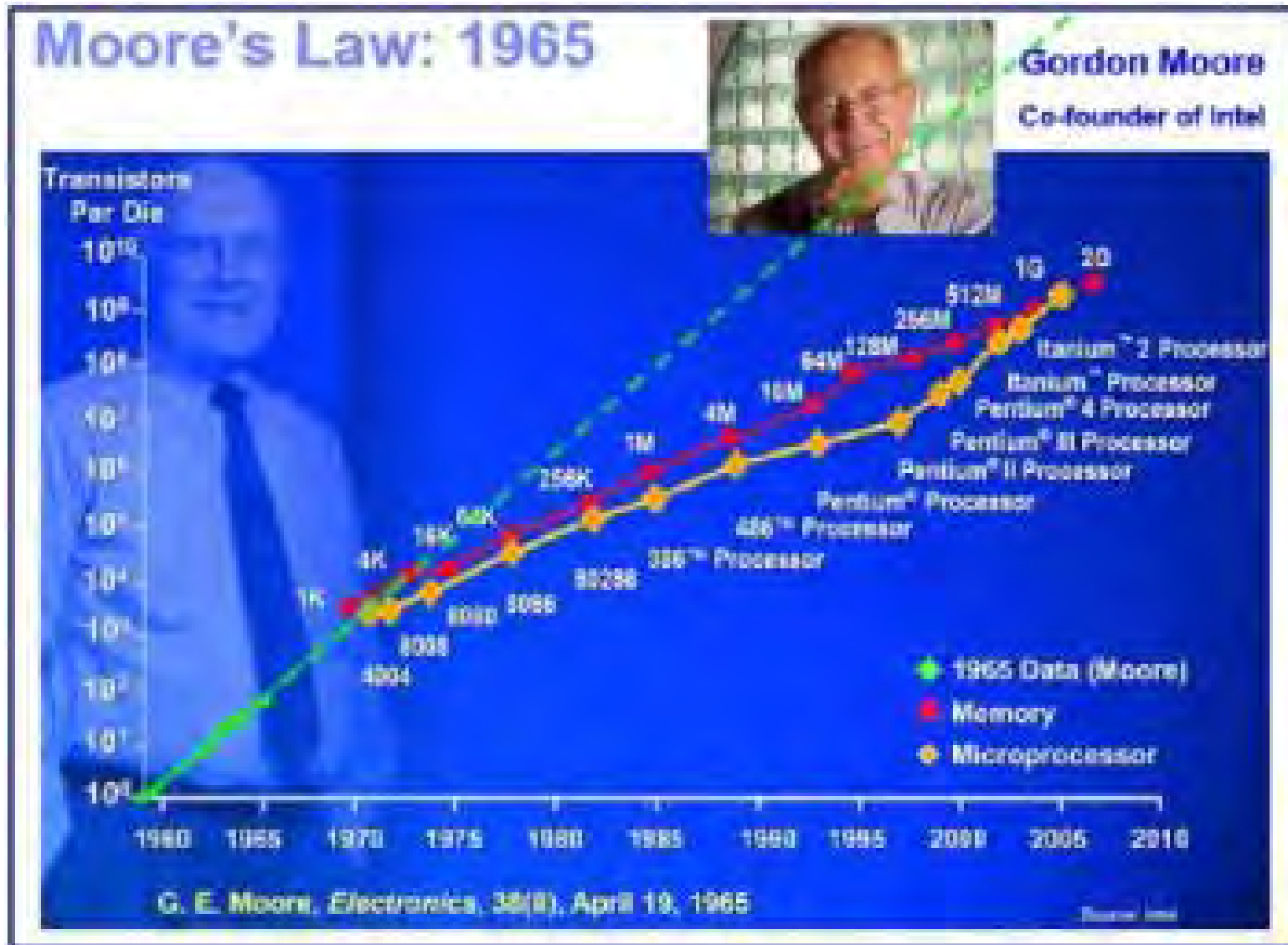
- The network models are used to make design performance trade-offs and architectural evaluations
- Traffic models used here are derived from trace data for the previous Arinc 629 based onboard network systems
- These models are used as a verification and configuration environment for the A3XX Network system
- Statistics collected include frame end-to-end latencies, switch latencies, switch memory requirements, switch throughputs, and switch queue statistics
- **Resulted in standards: ARINC 653, ARINC664P7**



Contract from Jean Paul Moreaux inventor of AFDX), Airbus and Honeywell

# MRD-3: Intel Architectural Optimization

TRAINING IN MR-MBSE in 1995 resulted in MOOR'S LAW GOING UP IN 1996



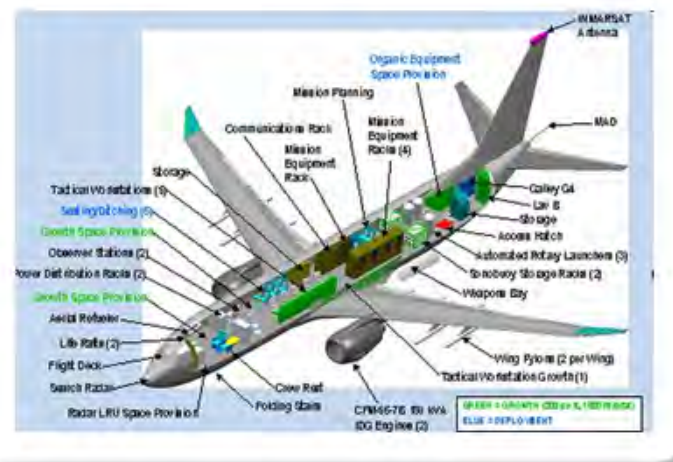


# MRD 3: NavAir P-8A Multi Mission Maritime Aircraft

## Providing modeling, simulation, and analysis technical support for B737 based P-8A Multi Mission Maritime Aircraft (MMA) Program

After model based design approach by BAH with Extend failed, 2005 MLD was called in to provide modeling support and tool

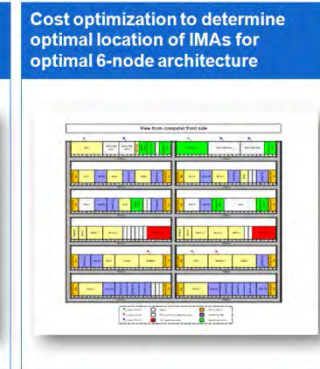
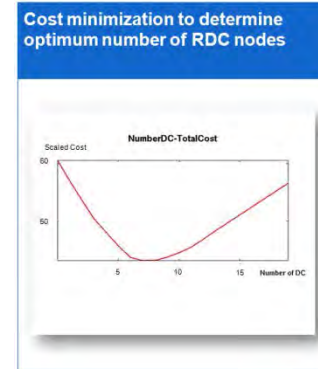
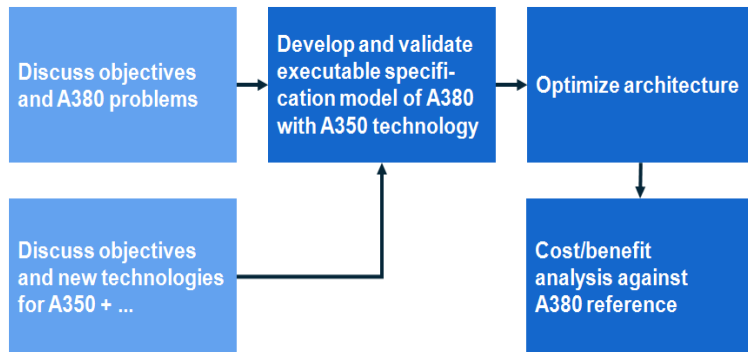
- Developed first executable specification of architecture, process and mission
- Formal executable specifications at aircraft level were introduced
- Model based architectural trade-off at mission/operational level
- Automatically generated html-files of models are integrated in LCM (Life Cycle Management System) and viewable in Browser over Intranet



### Result/Impact

Coverage analysis during flight test with MLDesgner based virtual prototype. Aircraft went into service in time without cost overruns. Life cycle management is carried out with virtual prototype. All first 100 missions were carried out faster or exactly as computed. NavAir gave Boeing followon contract to integrate sensors of 2 other aircraft in P-8, in order to be able to terminate the other aircraft.

# MRD 4: Aircraft level optimization of avionics architecture



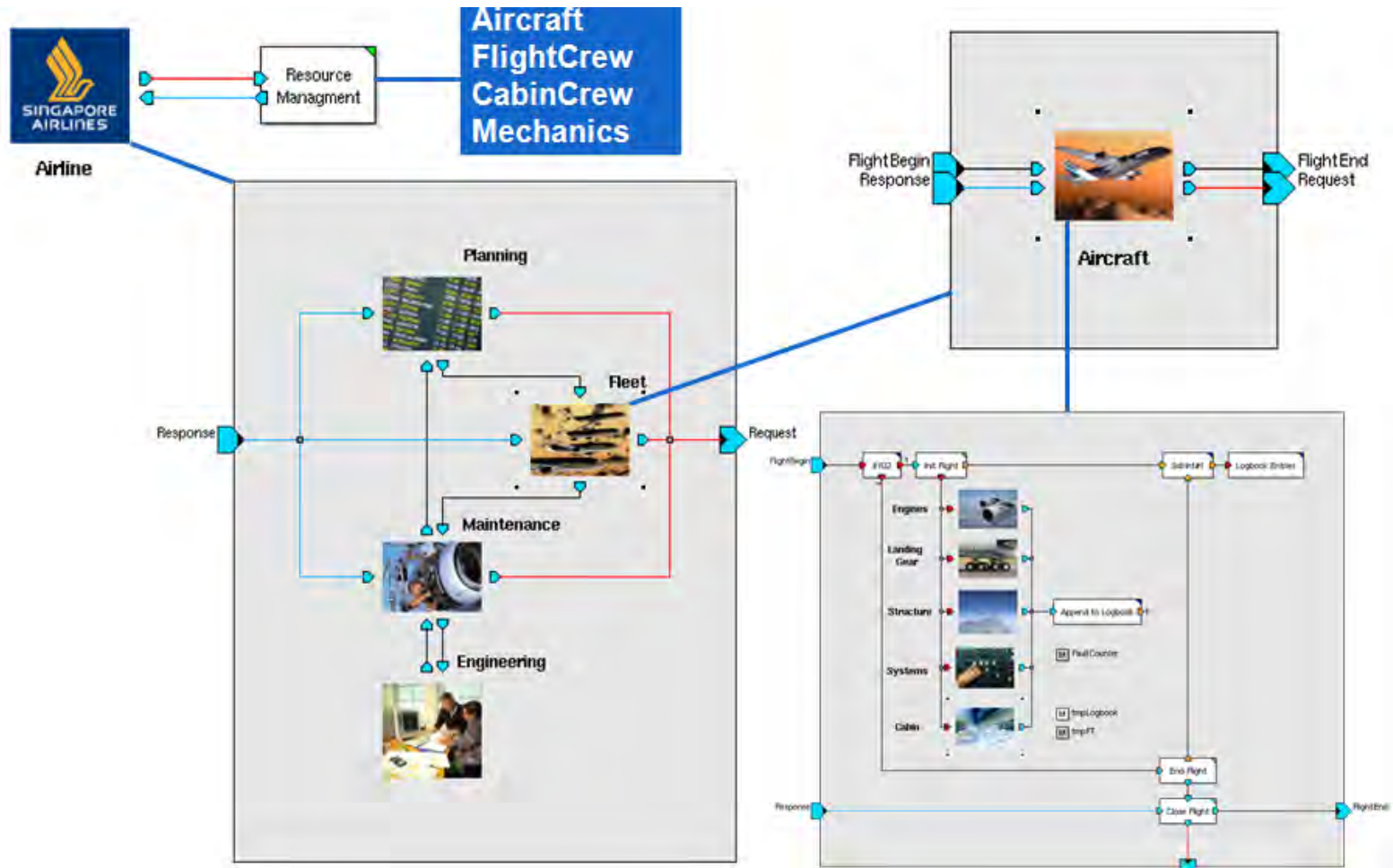
Performance item	Reference system	Optimized system
Cost of architecture [%]	100	28
Reduction of cables [%]		68
Weight of architecture [kg]	280.06	201.00
Weight of cables [kg]	108.21	29.15
Length of cables [feet]	1,531.46	453.84
MTBF [h]	55,632.88	57534.15
MTTR [h]	31.63	29.89
MTBUR [h]	49,164.46	50,874.30
Availability	0,99	0,9999999999999999

-\$1.77M/a/c

Further optimization w/new Airbus technology resulted in a switched architecture a/c design with x200 weight reduction of cables and >15% fuel saving + Airbus patents

# MRD 5: Optimize Aircraft Architecture/Maintenance/Operation for Transport Availability

Model aircraft down to LRU level, airline operation, and maintenance, including logistics



Optimization of A320 introduction at AA achieved 99.9% availability of first 25 of 260 aircraft.



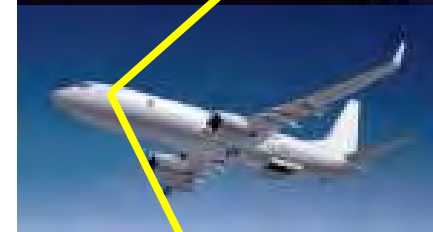
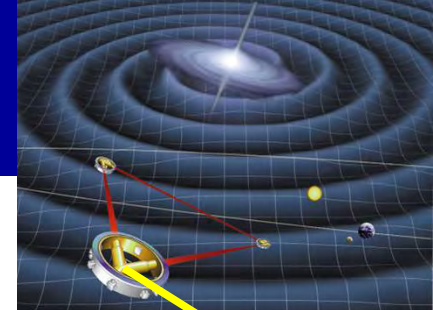
# REFERENCE CUSTOMERS

## SOME USERS OF MLDESIGNER

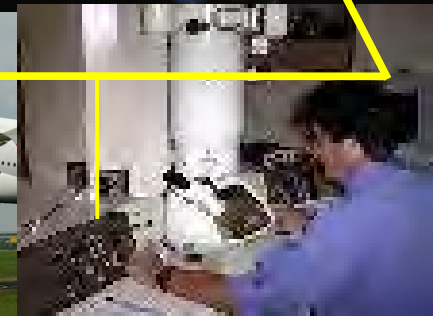
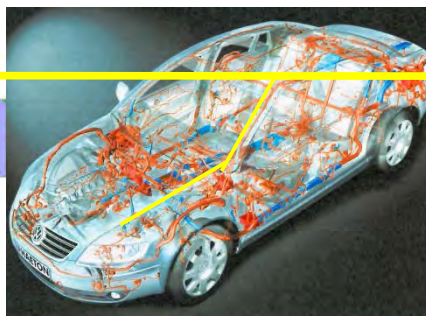
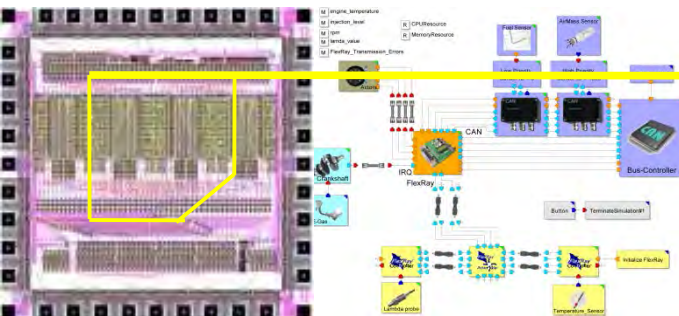


# MR-MBSE technology has been applied to design/analysis of:

- Networked systems
  - On Chip, Avionics, Aircraft, RPV, AUV, Satellites, Cars, Comm., Networked Computers
  - Complex software systems
  - Reconfigurable embedded systems, Software radios, ...
  - Multi-core architectures
  - Reconfigurable FPGAs
- Analysis, design and optimization of processes
  - Product design, development and quality processes
  - Logistics and SMC processes
  - Production processes
  - Organizational processes



**TETRA, GSM, GPRS, EDGE, UMTS, AFDX, USB, LIN, FlexRay, TTNT, QNT, 802.11<sub>a-n</sub>, 15, 16, SatCom**



# Summary

- It was shown that dynamically couples SoS of highest complexity and highest safety and security requirements can be developed without cost and time overruns using MR-MBSE technology
- For the example of aircraft development it was shown that this was only made possible by constant advancement in modeling, simulation and computer technology and close cooperation with domain experts
- It required
  - A small dedicated modeling and simulation team in the design office lead by an highly experienced engineer (Example: associated fellow)
  - A requirement of the executable specification/ virtual prototype to be a paid for deliverable (in MIL projects to customer)
  - A requirement that all integrations are first validated within the virtual prototype
  - All developers to have access to this virtual prototype, in order to be able to test disruptive new ideas for their impact on the system as used by the final customer/at mission level during design on the left hand side of the development **V**
- And permitted
  - Direct optimization for top level requirements at aircraft and service/mission level



# More Details

# Progress in MR-MBSE: 1980s

## Challenge

- Multi-disciplinary Aero-servo-elasticity problem in a/c & s/c
- ACOSS (LM/SAIC-SCT)
- F-16CD (AFWAL/LM), AMRAAM (EGLIN), Minuteman,...
- F-16Delta: Intergrated Flight-Propulsion control
- RADC Req. for Networked MIL/Aerospace electronics with dynamic architectures

## Solution

- SAIC developes unified language/tool CtrlC/ ModelC  
→ Matlab/Simulink
- SAIC: Frequency dependent weighting in optimal control
- SAIC: Validated executable specifications (VES)
- SAIC: VES of integrated FCS passed to subsystem developers -> F-22
- Cadence: BONEs

# Progress in MR-MBSE: 1990s

## Challenge

- B777, A380
- RASSP (Tri-Service)
- Arch. opt. of Intel chips
- Iridium, Globalstar, Teledesic, EOS, ...
- EOL BONEs
- MLD

## Solution

- VES of networked electronics with BONEs
- LM-ATL/Cadence: Unified methodology: Spec. with SPW/BONEs → 4x in 5Y  
Cannot solve DoD req for IoMT
- VES with BONEs (1995...)
- VES with BONEs/SatLab
- R/D for replacement >\$1B , ... did not work
- Cyberphysical sim MLDesigner

# Progress in MR-MBSE 2000s

## Challenge

- Aerospace Corp.
- TTNT (IoMT)
- P-8, Tanker a/c
- Intel Comm chips
- Airbus

## Solution

- BONEs → MLDesigner
- Rockwell Collins: OPNET → MLDesigner
- VES from concept development to LCM with MLDesigner
- VES for UMTS/GSM chip w/MLDesigner reduced iterations from 4x to  $\leq 1$  (dev time 9 month)
- A/C level and service level architecture optimization

# MLDesigner Software System

Common MLDesigner GUI, Block Diagram Editor  
Kernel, Simulation Control  
XML Model Description, CVS

## Design Domains

Discrete Event  
FSM/StateChart  
Static Data Flow  
Dynamic Data Flow  
Analog  
NS2  
Simulation Set  
New ...

## Libraries

Base Library  
Add-on Libraries  
802.11  
Network Lib  
Bus Systems  
...  
User Libraries

## Interfaces

MATLAB  
SatLab  
Mathematica  
Octave  
GSB  
Other Sim Tools  
Other Applications  
Hardware  
OpenGL  
Tcl/Tk  
Altia

## Conversion

BONeS => MLD  
COSSAP => MLD  
Ptolemy => MLD

# MLDesigner Software System

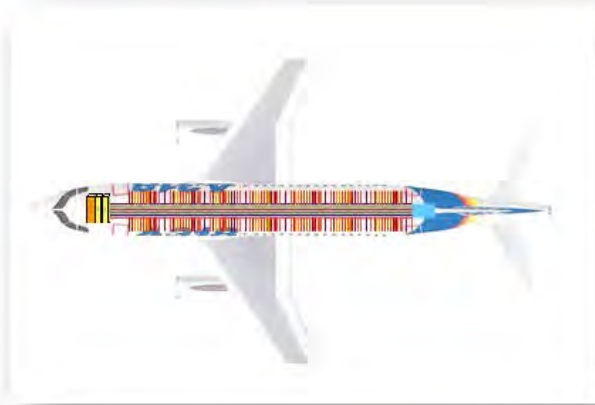
The screenshot shows the MLDesigner software interface with the following components labeled:

- Menu Bars:** Located at the top of the window, including File, Edit, View, Window, Settings, and Help.
- Tool Bars:** Located below the menu bars, containing various icons for file operations and editing.
- Tree View (Model Base):** A hierarchical view on the left side showing the project structure, including libraries like MLD\_DE\_Training and SYSTEMS.
- Context Menus:** Indicated by lines pointing to various elements in the interface.
- Model Editor:** The central workspace where a block diagram is being edited. The diagram includes blocks like PacketIn, Select Length, VarAndDelay#1, Switch#1, Packet with Errors Discarded, PacketOut, Input1 / ChannelSpeed, BinomialInt#1, and Bit Error Rate.
- Work Space:** The overall area where the model is being developed.
- Data Structure Editor:** A panel on the right side showing a list of data structures and their members.
- Data Structure Field Editor:** A panel below the Data Structure Editor for editing field values.
- Property Editor:** A panel at the bottom left showing the properties of the selected object (Channel), including Logical Name, Model Type, Author, Copyright, History, Version, Domain, Target, Hidden, Background Color, Default Bounding Vis., Default Icon, Import Libraries, Description, and Documentation.
- Console:** A panel at the bottom right showing the command prompt output, including version information and copyright notices.
- Status Bar:** Located at the very bottom of the window, displaying the current coordinates and zoom level.

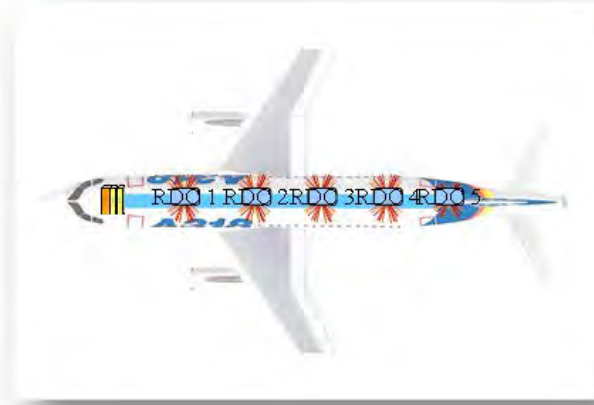


# Avionics Network Architecture Optimization: The Goal

Traditional Avionics Network Layout



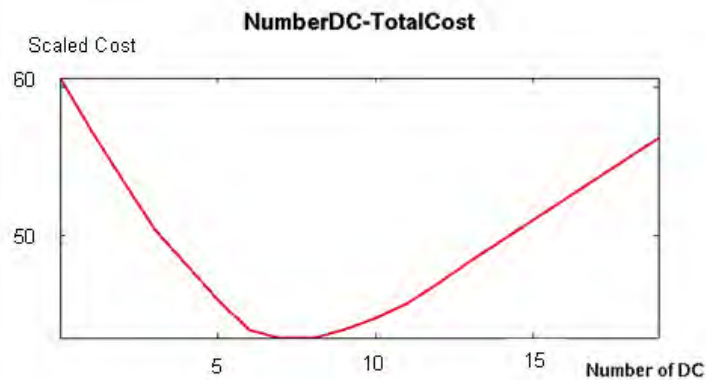
New Technology allows distributed Network layout



What is the minimum cost architecture that meets all requirements?

# Avionics Network Architecture Optimization

Cost optimization to determine optimum number of nodes



Cost optimization to determine optimal location of IMAs for optimal 6-node architecture



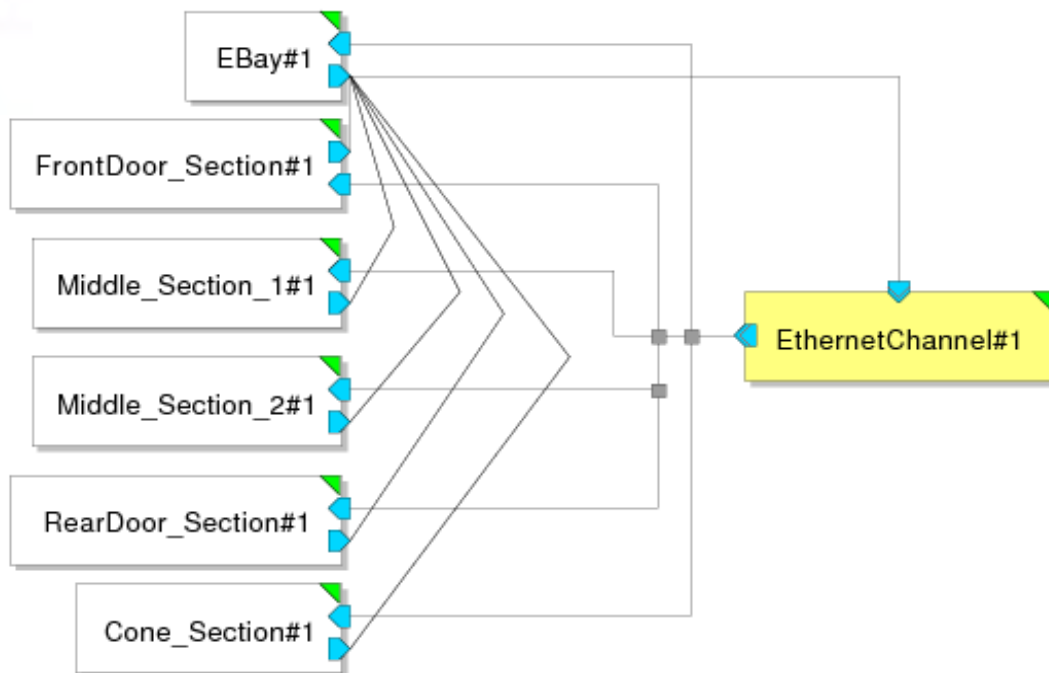
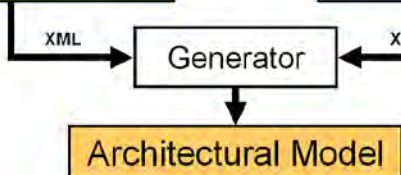
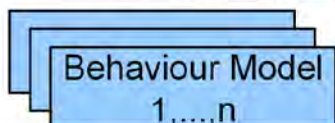
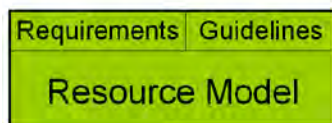
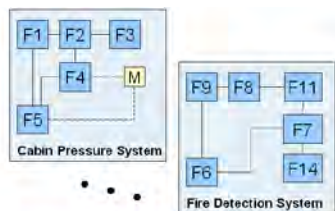


# Automatic generated model of avionics network architecture + function

## Mapping function into architecture

### • Ressources

- ▶ Aircraft properties
- ▶ Function / Equipment properties
- ▶ Cable routes, channel topology
- ▶ Cable type list



## Analysis of executable specification of optimized architecture of environmental system

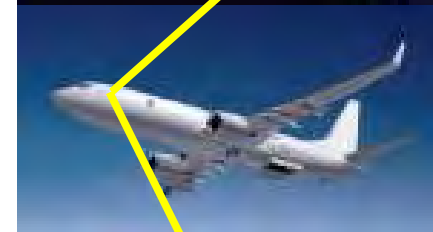
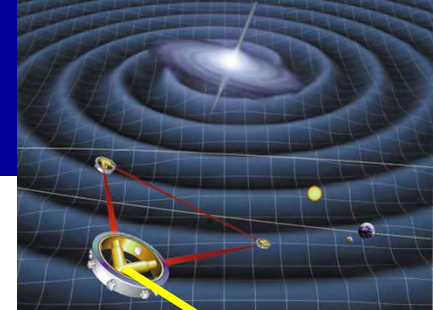
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MTBUR [h]	49,164.46	50,874.30
Availability	0,99	0,9999999999999999

**An optimized configuration will result in significant improvements in cost, weight, development time and risk level**

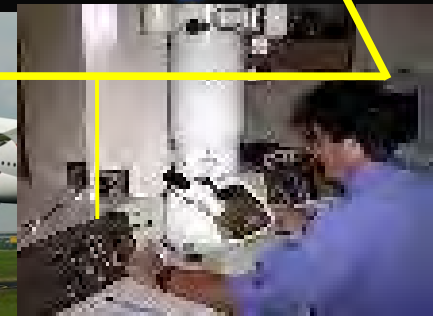
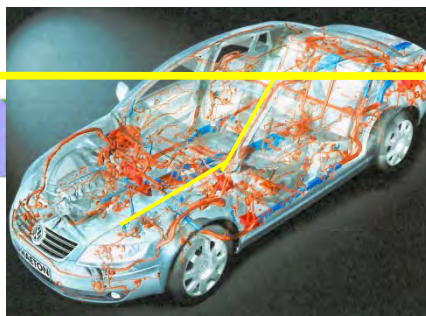
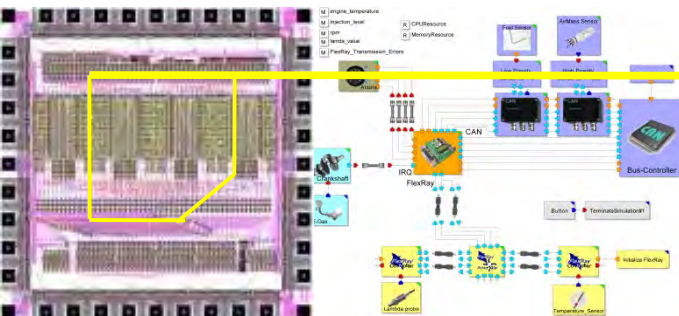


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# Contact

- Horst Salzwedel  
[horst@mldesigner.com](mailto:horst@mldesigner.com)
- Robert Sarkissian  
robert@mldesigner.com
- MLDesign Technologies  
2230 Saint Francis Drive  
Palo Alto, California 94303  
USA