

# Agile Hardware and Systems Process Proposal

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Will Bishop

6 May 2016

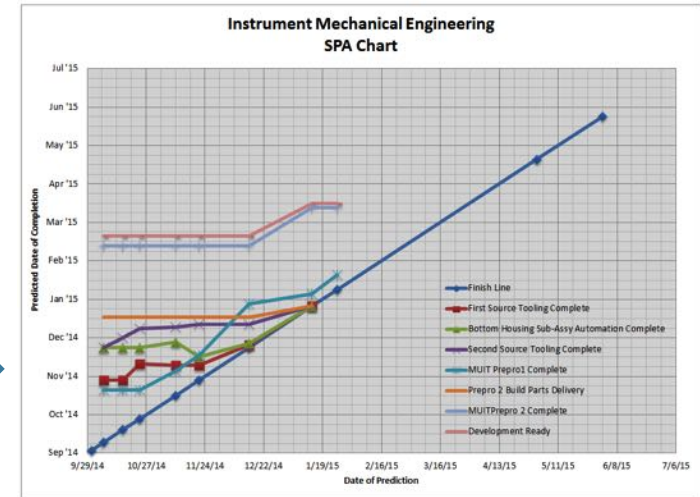
Roche Diabetes Care, Indianapolis Meter Development



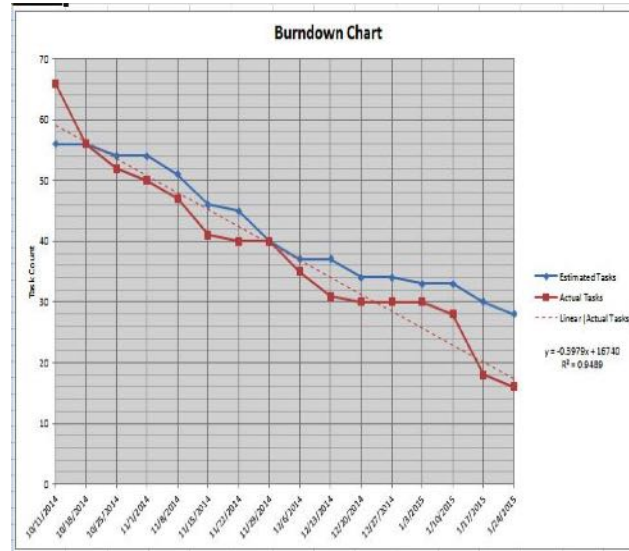
# Agile Hardware and Systems Proposal

	D	E	F	G	H	I
Task	Milestone Impacted	Progress Des. Date	Completion Date	Completion Date	Assign To	
1 Create fixture for measuring hooks on Bottom Housing (2-connection, 2-3-4-5)	First Source Tooling	-	-	10/10/2014	Akhil	
2 Research quality parameters around laser marking	Gen 2 R&D	12/12/2014			Akhil	
3 Create TIR for Tolerance Analysis	Gen 2 R&D	12/12/2014			Akhil	
4 TIR/MUT Report for DMH Mechanical Drop-Shock-Vibe Testing & Test Fixtures	First Source Tooling	11/12/2014			Akhil	
5 Create go/no-go fixture for Gen Link	First Source Tooling	-	-	10/10/2014	Akhil	
6 Update colors on Lens and Buttons drawings (standard to Japan)	First Source Tooling	-	-	10/10/2014	Caleb	
7 Update network files for Lens, Battery Door, Bottom Housing Sub-Assembly	Gen 2 R&D	10/10/2014		10/10/2014	Caleb	
8 Blue Folder for artwork files	Gen 2 R&D	12/12/2014	11/24/2014		Akhil	
9 Determine optimum extreme temp cycling timing	BTPLAT	10/10/2014	10/10/2014		Akhil	
10 Copy regulatory symbols to MEGROUP Labeling folder	BTPLAT	10/10/2014	10/10/2014		Akhil	
11 Create Geniva MS MUT Protocol - First Article Inspection & Capabilities Studies	BTPLAT	10/10/2014	10/10/2014		Caleb	
12 Create Geniva MS MUT Protocol - Material Certification	BTPLAT	10/10/2014	10/10/2014		Caleb	
13 Create Geniva MS MUT Protocol - Button Automation & Maximum Allowable Force	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Akhil	
14 Create protocol for TIR - Geniva MS MUT Battery Door Installation & Removal Force	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Janard	
15 Create protocol for TIR - Geniva MS MUT Screen Torque Seal	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Janard	
16 Create protocol for TIR - Geniva MS MUT Temp Resistance	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Akhil	
17 Create protocol for TIR - Geniva MS MUT Lens & Upper Marking Abortion	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Akhil	
18 Create protocol for TIR - Geniva MS MUT Battery Insertion & Removal	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Janard	
19 Create protocol for TIR - Geniva MS MUT Map Check Strip-Finger	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Akhil	
20 Create protocol for TIR - Geniva MS MUT Battery Door Seal	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Akhil	
21 Create Geniva MS MUT Protocol - Robustness Thermal Cycle	BTPLAT	10/10/2014	10/10/2014		Caleb	
22 Create Geniva MS MUT Protocol - Robustness Battery Door	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Caleb	
23 Insertion Cycle Test Plan	BTPLAT	10/10/2014	10/10/2014		Akhil	
24 Create Geniva MS MUT Protocol - Robustness Button Cycle	BTPLAT	10/10/2014	10/10/2014		Caleb	
25 Create Geniva MS MUT Protocol - Observations	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Caleb	
26 CN updates ID Spec document for colors (standard and Japan)	Gen 2 R&D	12/12/2014			Akhil	
27 Create protocols for Strip Connector MUT Plan	BTPLAT	10/10/2014			Caleb	
28 CN updates for MUT Plan	BTPLAT	10/10/2014			Caleb	
29 Draft memo to Project Records for BT tooling validations	BTPLAT	11/12/2014			Caleb	
30 PPAP approval of Negative Battery Contact	First Source Tooling	11/12/2014			Supplier	
31 SCF approval of Supplier parts	First Source Tooling	11/12/2014			Supplier	
32 Batteries Battery Door tool	First Source Tooling	11/12/2014			Supplier	
33 Battery Door tool validation	First Source Tooling	11/12/2014			Supplier	
34 BT Completion Report	BTPLAT	11/12/2014			Caleb	
35 Bottom Housing tool validation	First Source Tooling	10/10/2014	10/10/2014		Caleb	
36 Bottom Housing Sub-Assembly Automation FAT	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Supplier	
37 Bottom Housing Sub-Assembly Automation Site Install	BTPLAT	10/10/2014	10/10/2014	10/10/2014	Supplier	
38 Bottom Housing Sub-Assembly Automation SAT	BTPLAT	11/12/2014			Supplier	

Example  
Information flow:  
Sprint Task  
Tracking to  
Burndown  
and to SPA



Task Tracking List



Top-Level  
SPA Chart

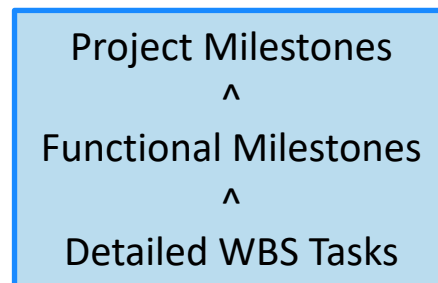
Prochain  
Input

# *Agile Hardware and Systems Proposal*

## *Functions and Project Schedule*

Hardware Agile aligns with other Agile efforts for a Systems Agile approach

- HW, FW, and other sprints align to support builds, events and milestones
- Data from E.V. task tracking and burndowns for schedule and budget for each function is summarized in project level information including SPA charts



# Agile Hardware and Systems Proposal

## Functions and Project Schedule

Diagram of an Agile Project												
Tool	Main Audience	Content										
Track in SPA Charts	Team, Leadership, Senior Leaders	Project Milestones >	Proto 1	Proto 2	Concept Final	Human Factors	Dev Model 1	Dev Model 2	Design Complete	Prepro 1	Prepro 2	Development Complete
WBS, Burndown, and Retrospective Tracking	Team, Leadership	Firmware Sprint >	F1	F2	F3	F4	F5	F6	F7	F8		
		Test stands Sprint >	T1	T2	T3		T4	T5	T6			
		Hardware Sprint >	H1	H2	H3		H4	H5	H6		H7	
High Level Functionality Planning Table	Team, Leadership	Functions and Deliverables	Function 1 Function 2	Function 3	Function 4 Concept Deliverable	Function 5 HF functions	Function 6 Prototype tooling	Function 7	Function 8 Design deliverable	Function 9 Production process Validated tooling	Function Y UIT	Function Z

# *Agile Hardware and Systems Proposal*

Agile Tools currently in development or use for hardware are:

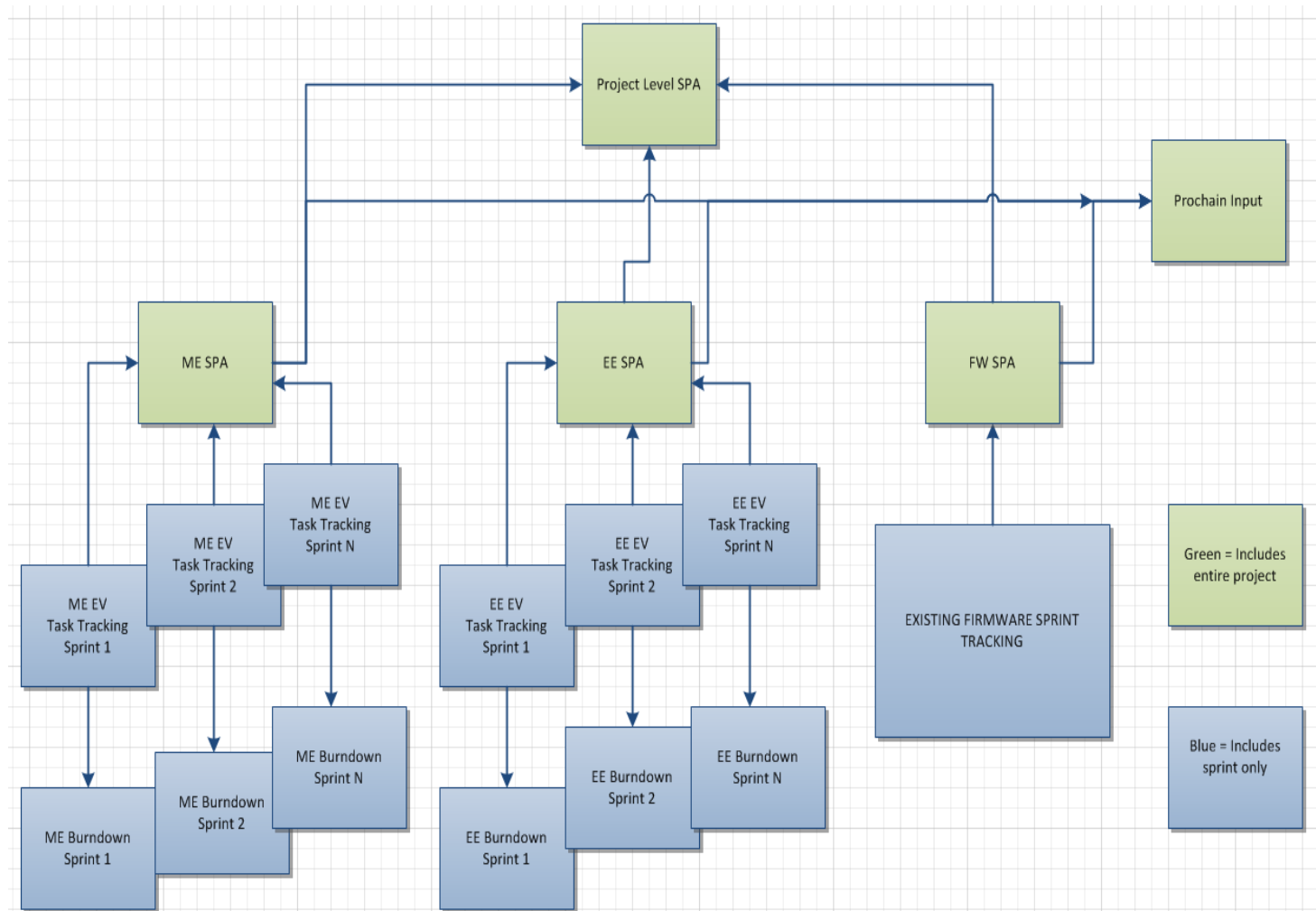
- Task lists/Work Breakdown Structures
- Sprints and Burndown charts
- Schedule Prediction Accuracy (SPA) Charts

How does hardware Agile relate to software/firmware Agile and Agile in general?

- In our process, hardware sprints do not have a fixed length; the recommendation is to have a firm functional or milestone goal for the hardware sprint, such preparation for an upcoming build. The end date of the sprint floats with the completion of that goal.
- Hardware sprints are normally longer than software sprints

# Agile Hardware and Systems Proposal

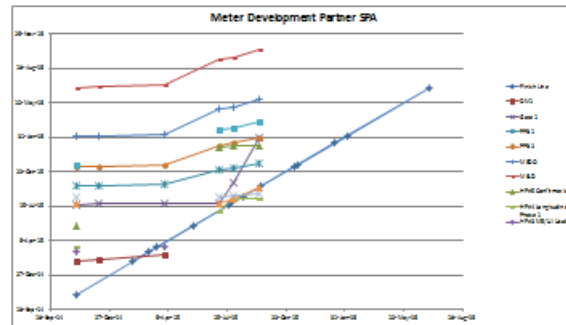
The tracking efforts for all functions can be aligned in to an Agile project structure



# Agile Hardware and Systems Proposal

## Example Elements of an Agile Project - Outsourced

Plan #	UIT Task	Task Description	Task Conditions	Star	Responsibility	Total Effort Estimate		Work Remaining, hours			
						Total Estimate, hours	Planned Completion Week	Week 1	Week 2	Week 3	Week 4
New	New Leakage Test	high temp & high humidity	Default Condition	None	None	4	3	0	0	0	0
New	New Leakage Test	high temp & high humidity	Default Condition	None	None	12	4	12	0	0	0
New	New Leakage Test	high temp & high humidity	Default Condition	None	None	4	5	4	4	4	0
UIT	UIT KIT ASSEMBLY	Assemble Test Fixture	Default Condition	None	None	4	1	4	0	0	0
UIT	UIT KIT ASSEMBLY	Assemble Test Fixture	Default Condition	None	None	0	4	0	0	0	0
UIT	UIT KIT ASSEMBLY	Assemble Test Fixture	Default Condition	None	None	4	6	2	2	2	1
NA	Order Parts	NA	NA	NA	NA	20	6	20	20	20	20

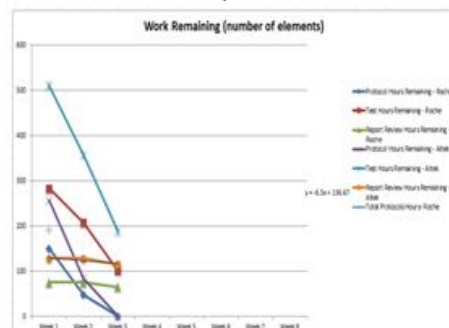


For an outsourced project, how to track inside vs. outside work?

- For inside and outside UIT, keep detailed EV Task Tracking/Burndown
- Other tasks: Have partner report functional SPA details, which will feed into reporting structure. Plus other reports from their system

EE Tracking with EV  
—Internal and UIT

External Partner Functional SPA



EE Burndown of EV  
—Internal and UIT

EE SPA -  
Combined

ProChain

Project SPA

EV = Earned Value

# *Agile Hardware and Systems Proposal*

## Future enhancements:

- Show WBS structure containing multiple sprints, and backlog items moving with EV from sprint N to sprint N+1
- Show flow of EV from WBS to functional summary to project, in Excel format across project and time



# *Agile Hardware and Systems Proposal*

## References:

- [www.tcgen.com](http://tcgen.com/agile-software-hardware-part-3/#.Vk3s0KQo57g) : <http://tcgen.com/agile-software-hardware-part-3/#.Vk3s0KQo57g>
- Scrum in Mechanical Product Development: Thesis ÞÓRDÍS REYNISDÓTTIR, Chalmers University of Technology, Gothenburg, Sweden, 2013

*Thank You*

