



28th Annual **INCOSE**
international symposium

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Property Modelling Methodology (PMM®)

*A disruptive MBSE approach to digital continuity
in systems development*

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Property Model Methodology: *A disruptive approach to master system developments*



- 1) Airbus Helicopters Stakes & Challenges
- 2) PMM overview
- 3) Early specification elaboration & validation
- 4) Collaborative System design architecture
- 5) Requirement refinement & validation
- 6) Detail design models
- 7) Verification process
- 8) Synthesis & perspectives



Property Model Methodology:

1) *Airbus Helicopters main challenges*

- **Improve H/C development efficiency** (leadtime & NRC) to:
 - ✓ maintain its current **market share**
 - ✓ Make room for extending to **additional product** (drones, SoS,...)
- ...while managing **complexity** ▶
 - ✓ **Various missions** to accommodate on same H/C platform
 - ✓ **High density** of functions to integrate
 - ✓ Helicopter domain **specificities**
 - ✓ Unmanned flight **specificities**



Property Model Methodology:

2) PMM overview (1/3):

*Process architecture **driven by ARP4754A goals***

➤ Modeling & Simulation method with coherent steps

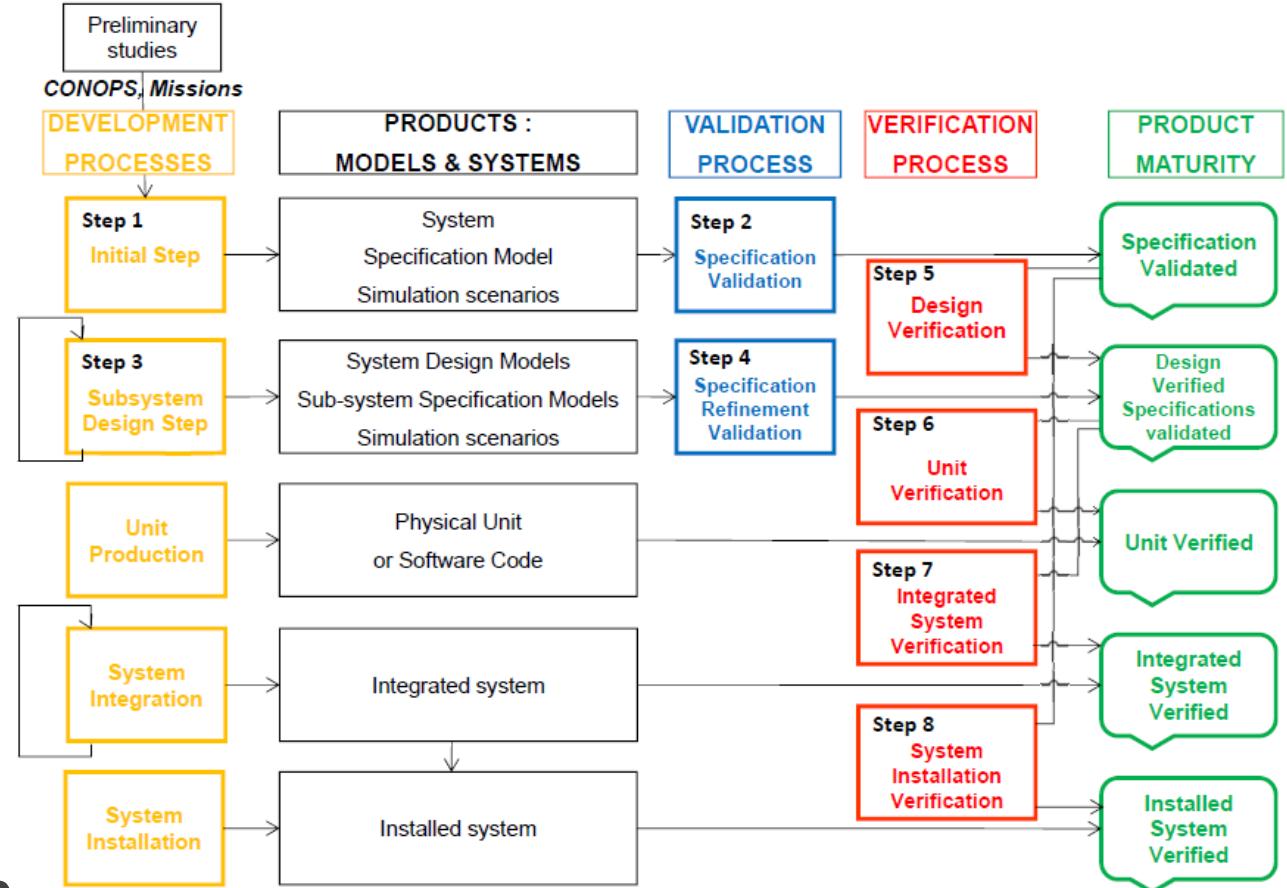
1. Specifying the right system:

- a) Specification model production
- b) Specification model validation:
 - i. Formal validation
 - ii. Factual validation

2. Designing the system right:

- a) Architectural design model
- b) Requirement refinement and validation
- c) Physical design models
- d) Design verification

3. Verifying physical subsystems & integrated system

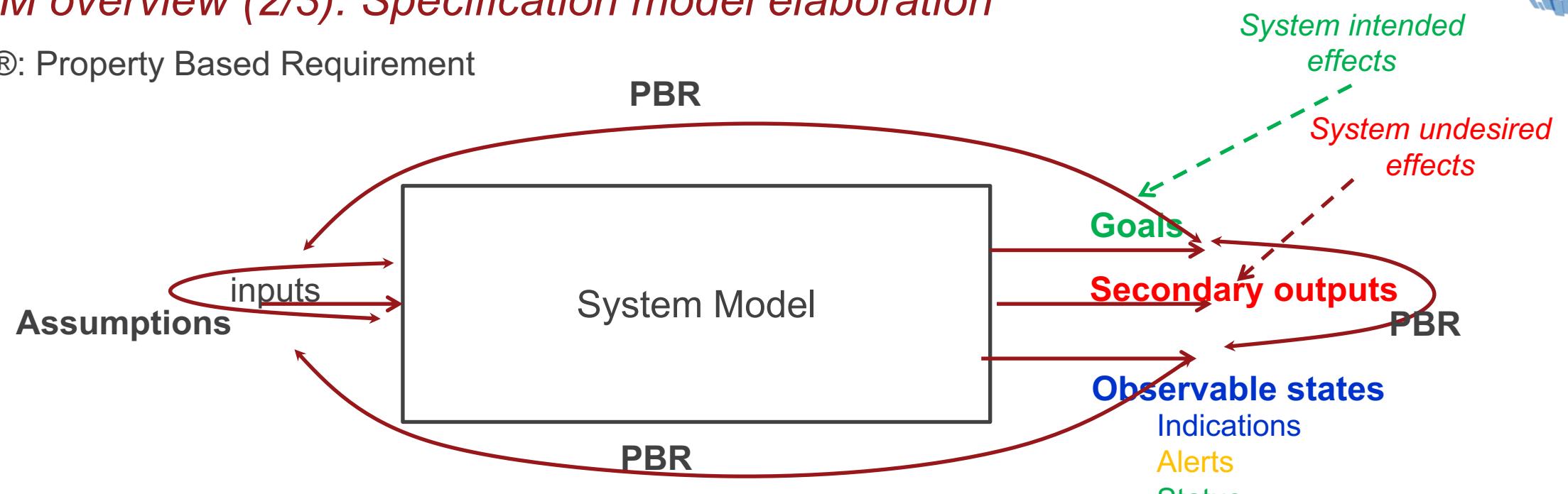




Property Model Methodology:

2) PMM overview (2/3): Specification model elaboration

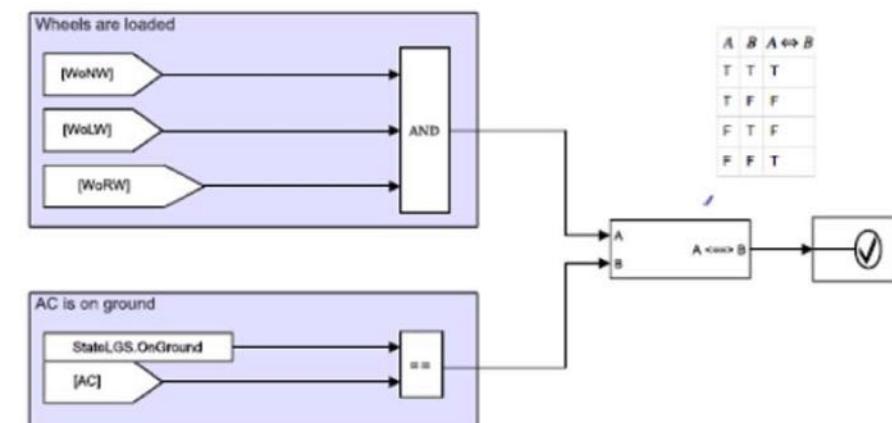
PBR®: Property Based Requirement



Specification modelling sequence (**PBR**) is a backward process from the effects to the causes:

When $C \rightarrow \text{val}(O.P) \in D$

When **condition C** is true, **property P** of **object O** is actual and its value shall belong to **domain D**



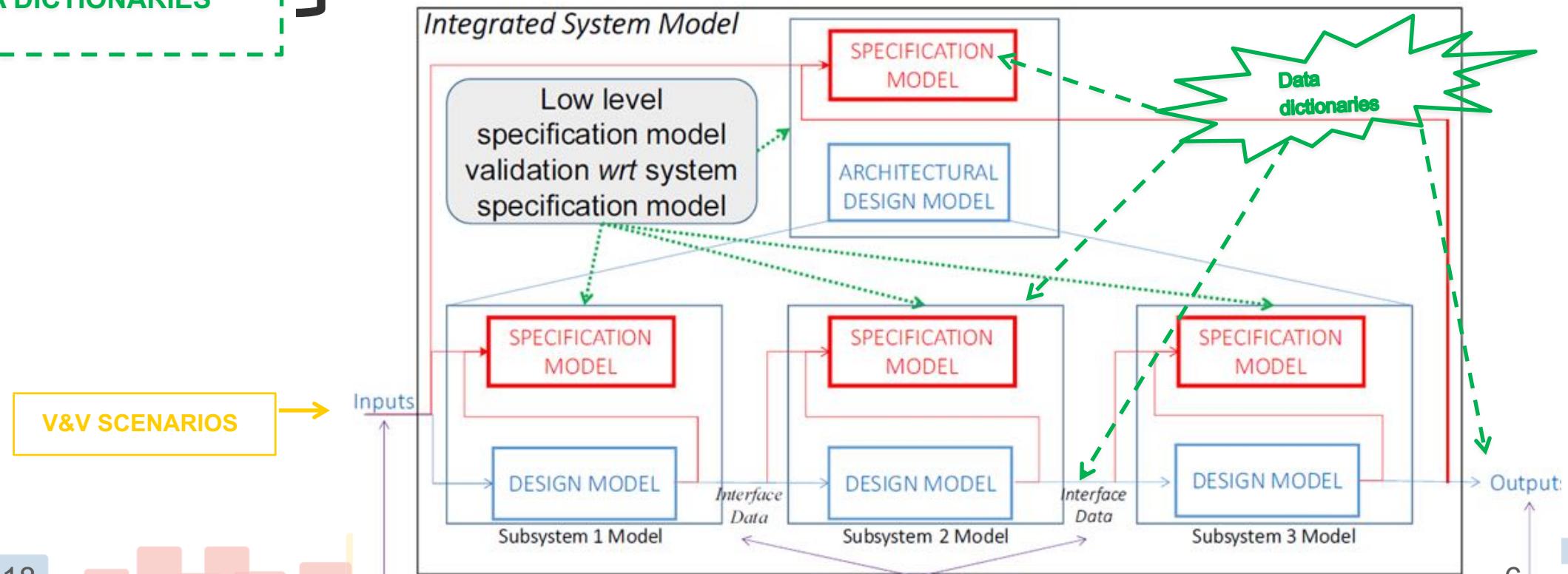
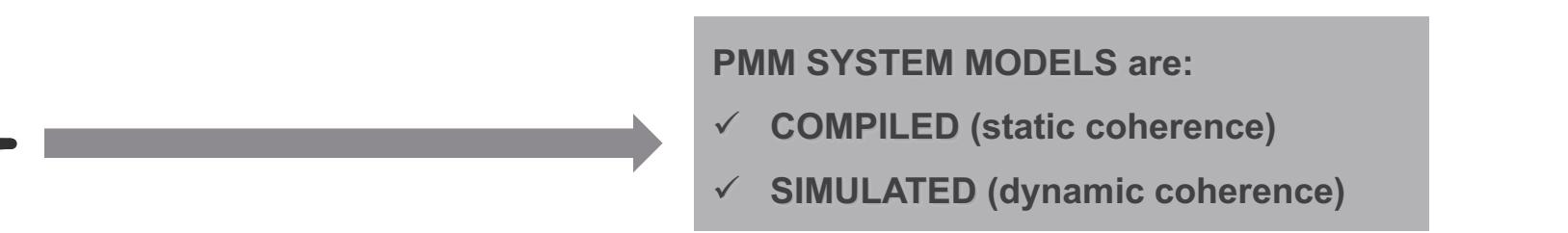


Property Model Methodology:

2) PMM overview (3/3): Digital product

➤ A SYSTEM MODEL: CONFIGURATION OF 4 TYPES OF DIGITAL ENTITIES

- ✓ **SPECIFICATION MODELS**
- ✓ **DESIGN MODELS**
- ✓ **V&V SCENARIOS MODELS**
- ✓ **DATA DICTIONARIES**





Property Model Methodology:

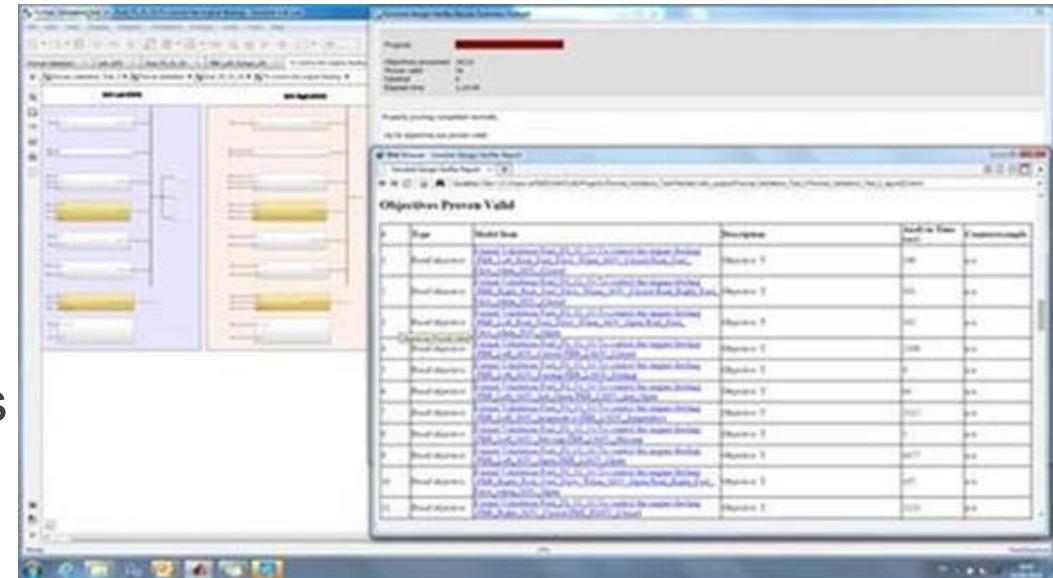
3) *Early specification validation(1/2): Formal validation*

➤ **Goal** is to remove from specification models:

- ✓ **Logical holes** (logical completeness)
- ✓ **Logical contradictions** (logical correctness)

➤ **Way:**

- ✓ Instrumentation Resource : proof means
- ✓ Human Ressource: **Modeler**
- ✓ Computation Ressource: **SLDV prover**



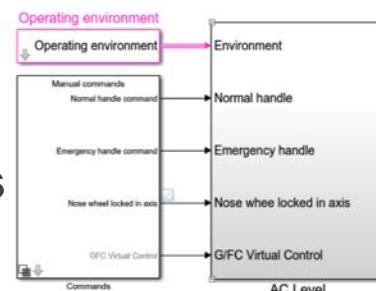
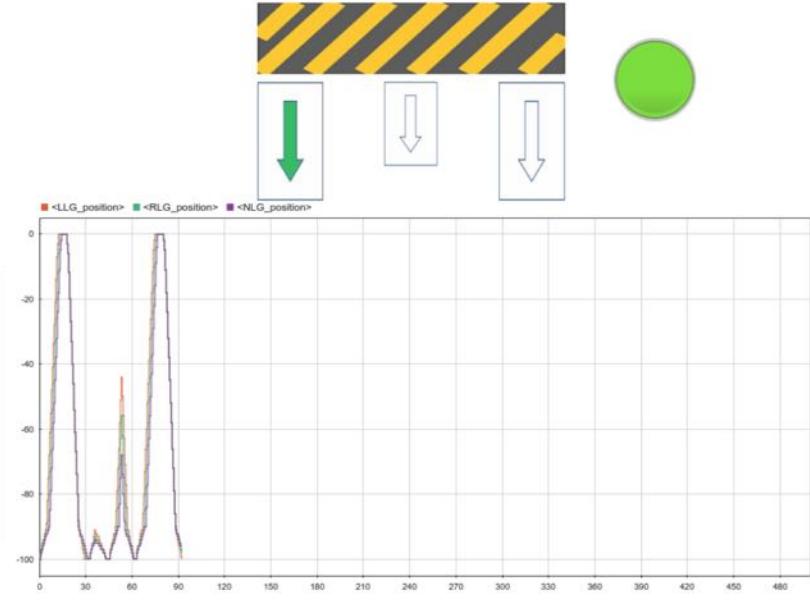
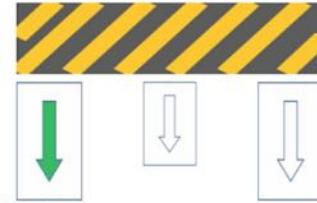


Property Model Methodology:

3) Early specification validation(2/2): **Factual validation**

➤ **Goal** is to reach an agreement among relevant stakeholders on

- ✓ Factual completeness: stakeholders **jointly** consider **all needs covered**
- ✓ Factual correctness; → stakeholders **jointly** consider **simulated behaviors** are the **right ones**





Property Model Methodology:

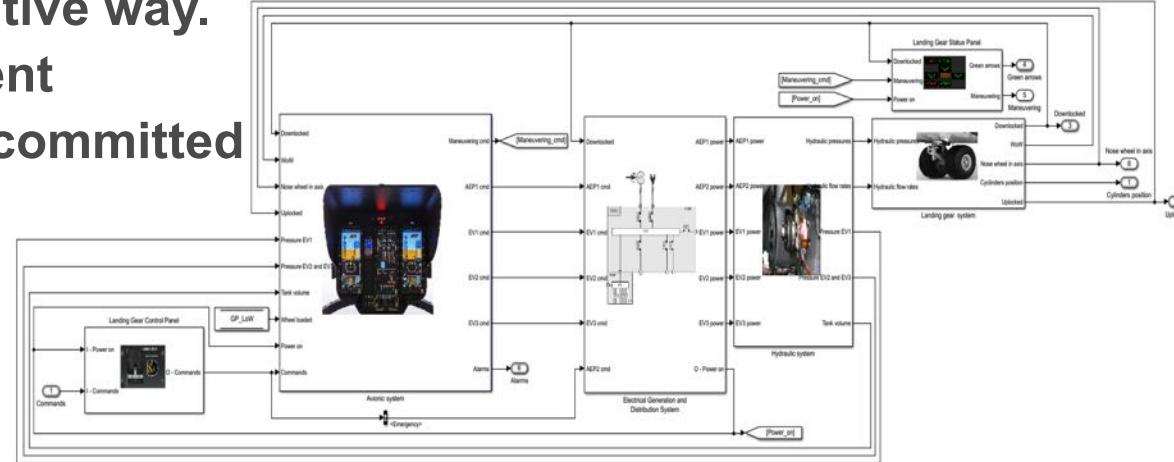
4) Collaborative system design architecture

➤ Goals

- ✓ Functional chains are established in a **collaborative way**.
- ✓ Interfaces are strictly defined and fully coherent
- ✓ Roles & responsibilities of each contributor are committed

➤ Way:

- ✓ System **architecture** is modeled
- ✓ System **internal interfaces** are collected in a **shared interface dictionary**
- ✓ System **specification** model is refined in **subsystem** specification models
- ✓ **Subsystem vs System** specification model **completeness and correctness** are established by **simulation**.





Property Model Methodology:

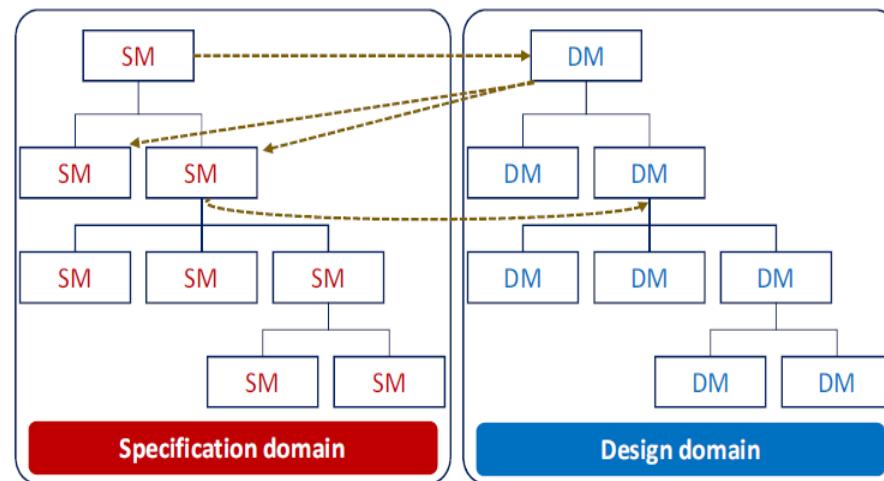
5) *Requirement refinement validation*

➤ Goals:

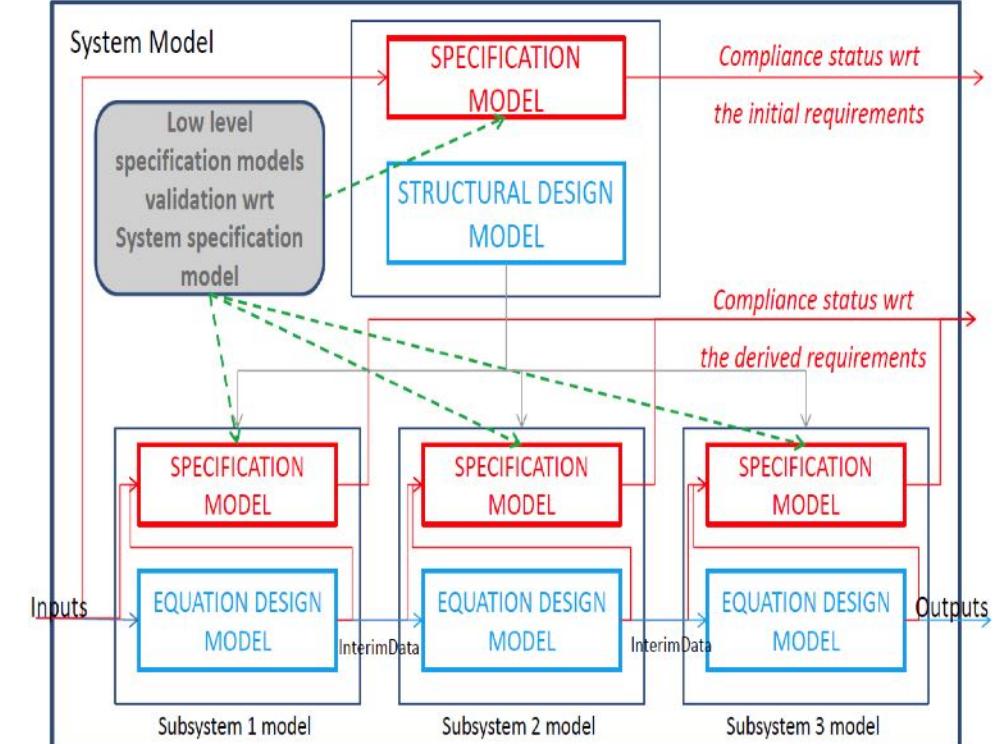
- ✓ to **refine** system **requirements** into subsystem requirements
- ✓ to **validate** the refinement (system req -> subsystem req)

➤ Way

- ✓ Refinement based on Suh's **zig zagging** concept
- ✓ Validation : SLDV prover & simulation



Zigzagging (Suh, 02)



When $DM \rightarrow PBR \leq PBR_1 \wedge \dots \wedge PBR_n$



Property Model Methodology:

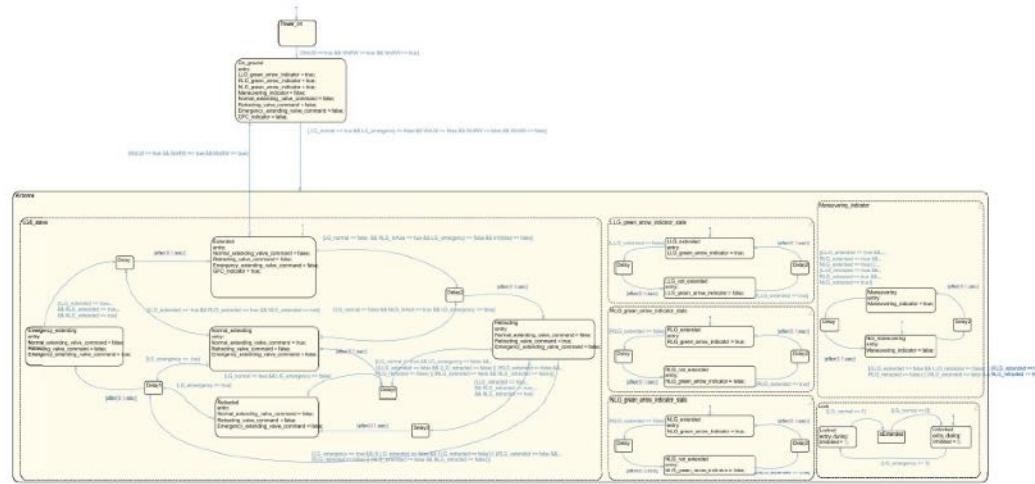
6) Detailed design models

➤ Goals:

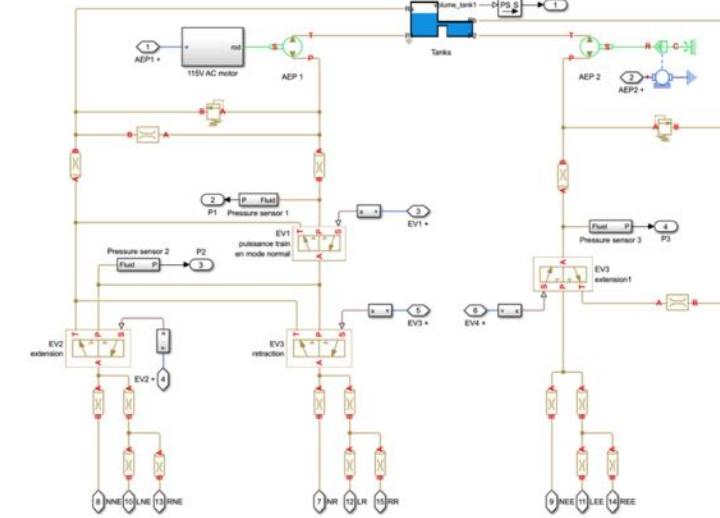
- ✓ to define a detail design for the component subsystems

➤ Means

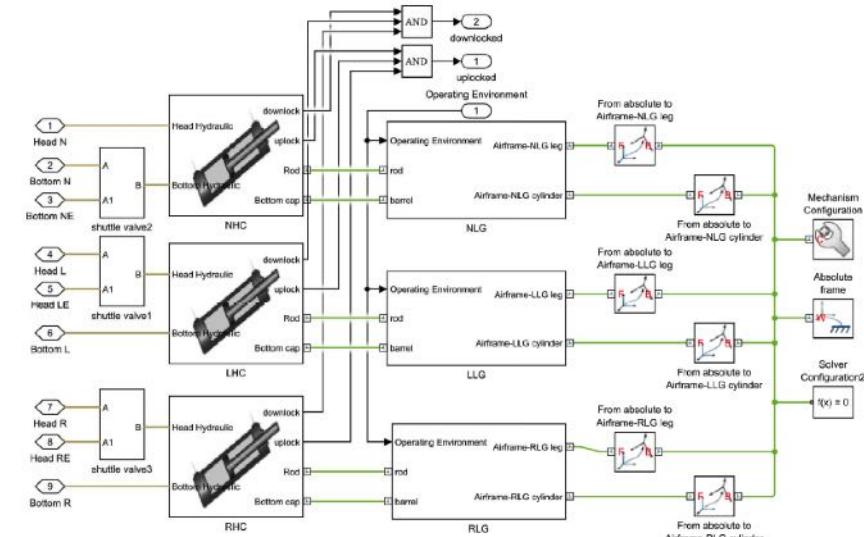
- ✓ Language resources : Stateflow®, Simscape®, Scade®, Adams® ...
- ✓ Relevant design specialists



Stateflow design of an avionic subfunction



Simscape design of an hydraulic subfunction



Simscape design of an mechanical subfunction



Property Model Methodology:

7) *Verification process (1/3)*

➤ **Goals:**

- ✓ At any stage of the product, implementation is verified
- ✓ Design is verified **before** production to remove design errors
- ✓ Equipment, integration and installation is verified to equipment, integration and installation errors

➤ **Way**

- ✓ Design models are verified by simulation regarding to their specification models .
- ✓ equipment, integration and installation are verified by test regarding to the specification models



Property Model Methodology:

7) Verification process (2/3)

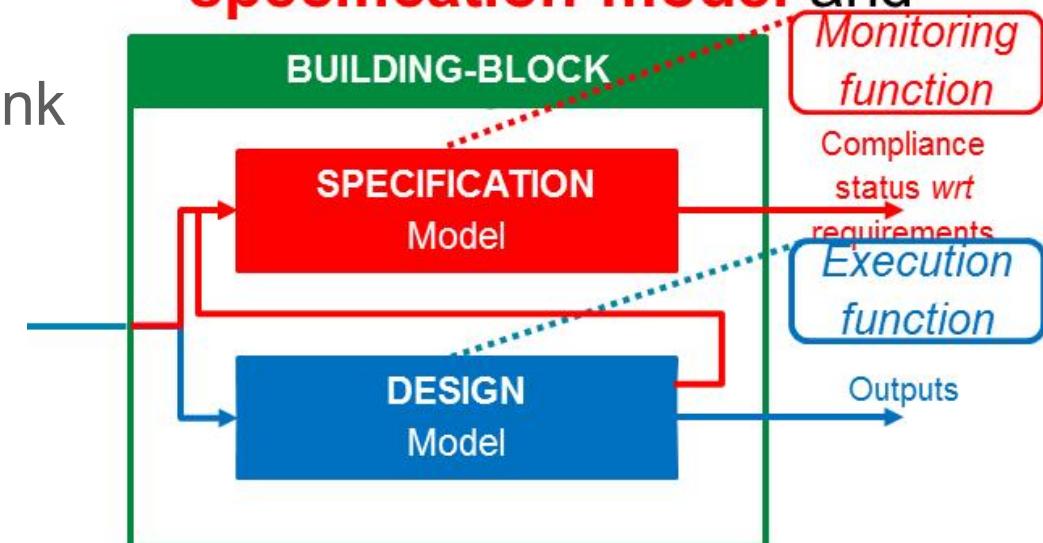
➤ Goals:

- ✓ to ensure that there is no **design error** at any level of the design hierarchy

➤ Means

- ✓ Human Ressource: subsystem resp
- ✓ Computation Ressource: Matlab/Simulink
- ✓ test cases
 - from SLDV test generator
 - Flight test recorded data
- ✓ Verification runs and results analysis

« **Co-simulation** » of **specification model** and





Property Model Methodology:

7) Verification process (3/3)

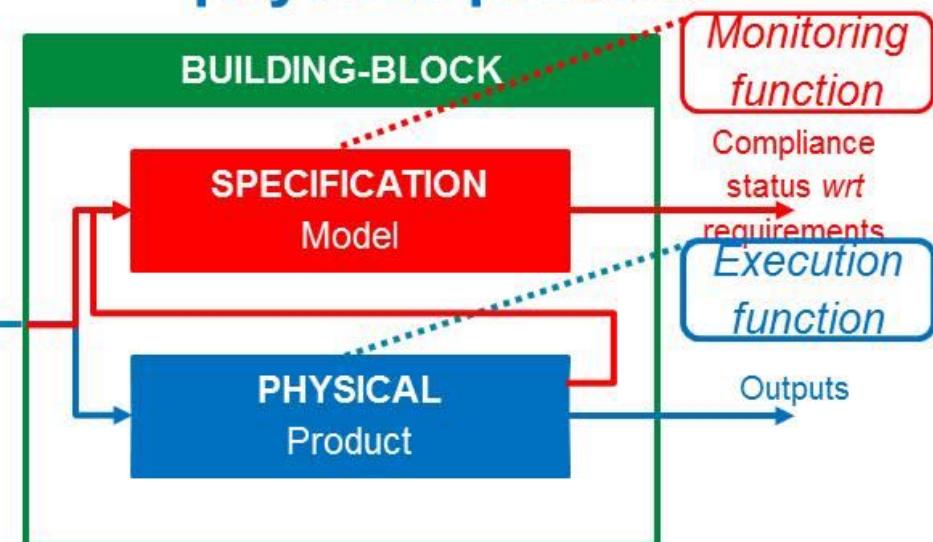
➤ Goals :

- ✓ to ensure that there is no production or physical integration errors at any level of the product hierarchy

➤ Means (Not yet experimented)

- ✓ Human Ressource: test responsibles
- ✓ Test Resource: Test rigs
- ✓ Computation Resource : Simulink
- ✓ Validation and verification scenarios and cases collected during specification and design phases
- ✓ Verification runs and results analysis

« **Co-simulation** » of
specification model and
physical product

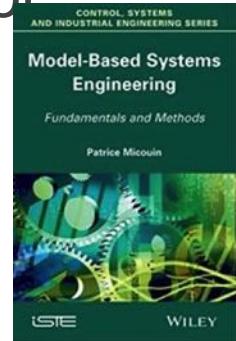


Property Model Methodology:

8) *Synthesis & perspectives (1/3)*



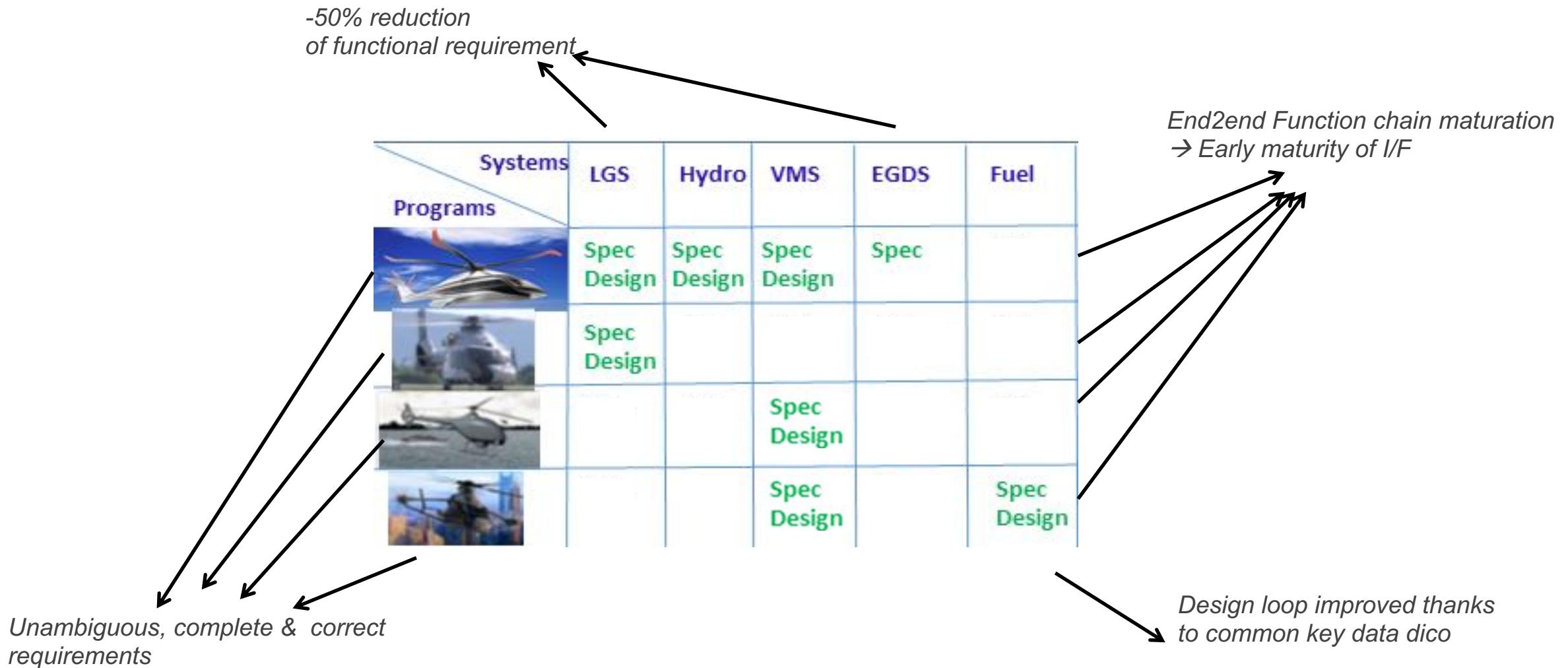
- ✓ A method (i.e. concepts + strictly defined development process for continuous, discrete and hybrid systems)
- ✓ Follows the **ARP 4754A** recommendation for A/C development
- ✓ Follows Systems Engineering and Parsimony Principles
- ✓ User and Goals Oriented
- ✓ Centered on Engineering Disciplines
- ✓ Model and simulation based approach
- ✓ **Framework for Digital Continuity of Development Products**
- ✓ An approach language- and tool- agnostic
 - Currently implemented in several simulation languages





Property Model Methodology:

8) *Synthesis & perspectives (2/3)*



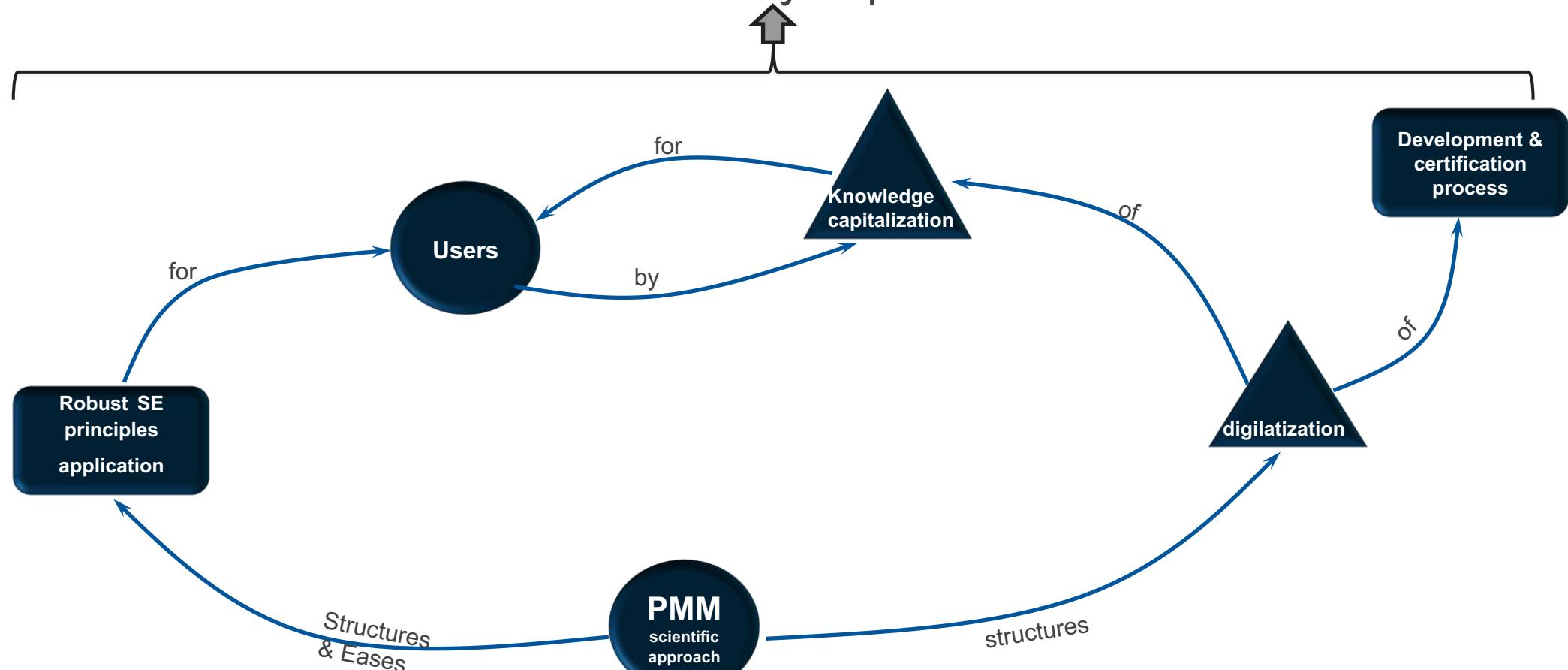
Property Model Methodology:

8) Synthesis & perspectives (3/3)



Global improvement in A/C development efficiency

Collective users efficiency improvement





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