



Powering Innovation That Drives Human Advancement

**Real-time collaborative system modelling with
Ansys System Architecture Modeler (SAM)**

Michael Soden / Senior Product Manager

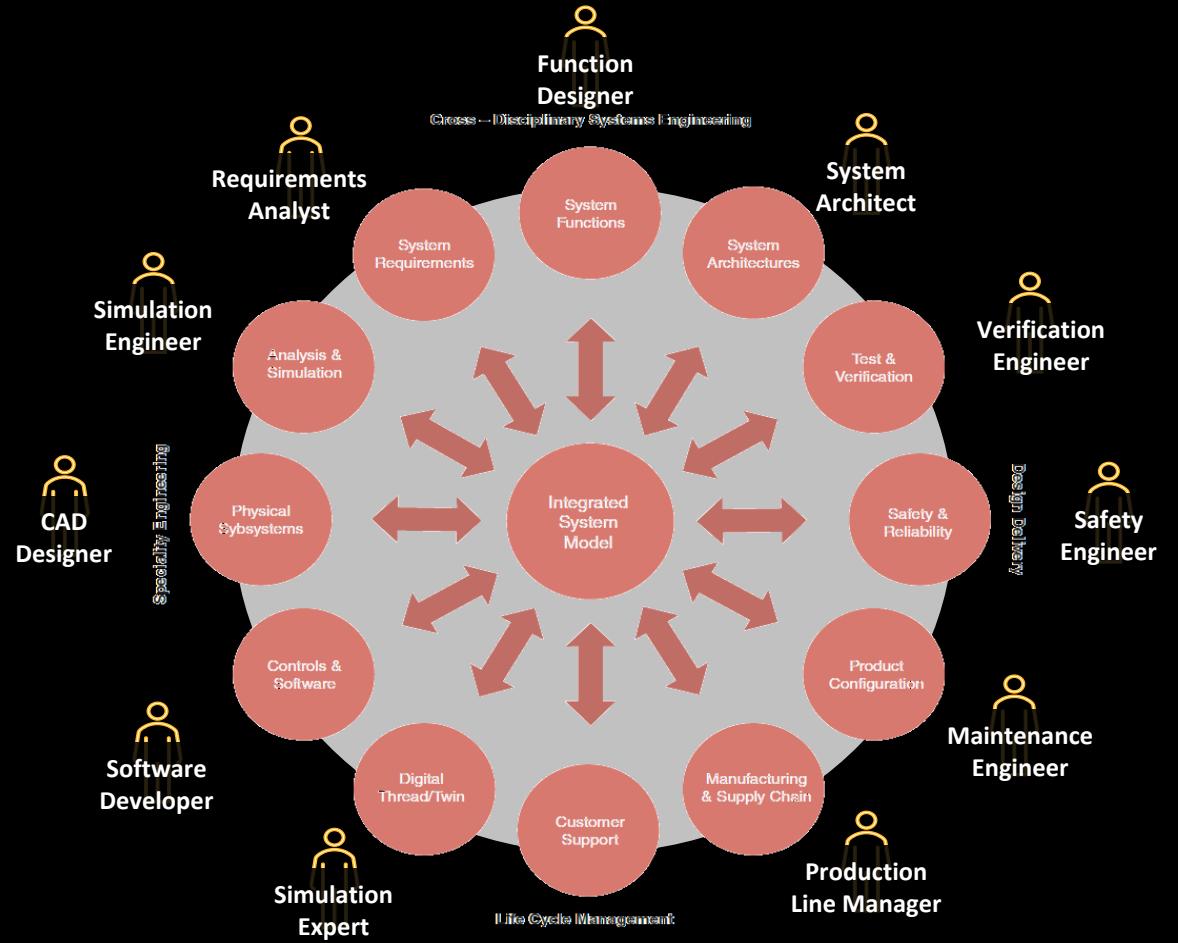
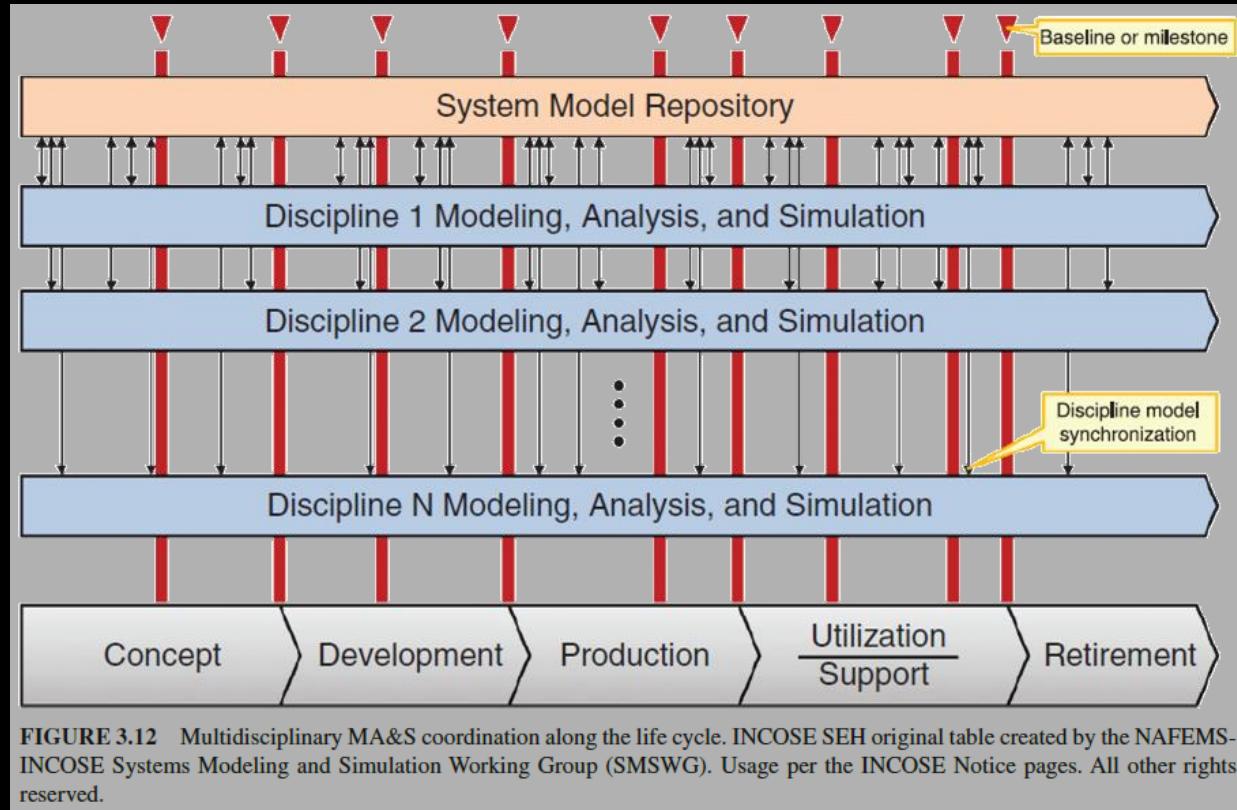
2024-11-23

Agenda

- MBSE and Digital Engineering at Ansys
- System Architecture Modeler (SAM)
- Q&A



Challenge: Model-based Integration across multiple Disciplines

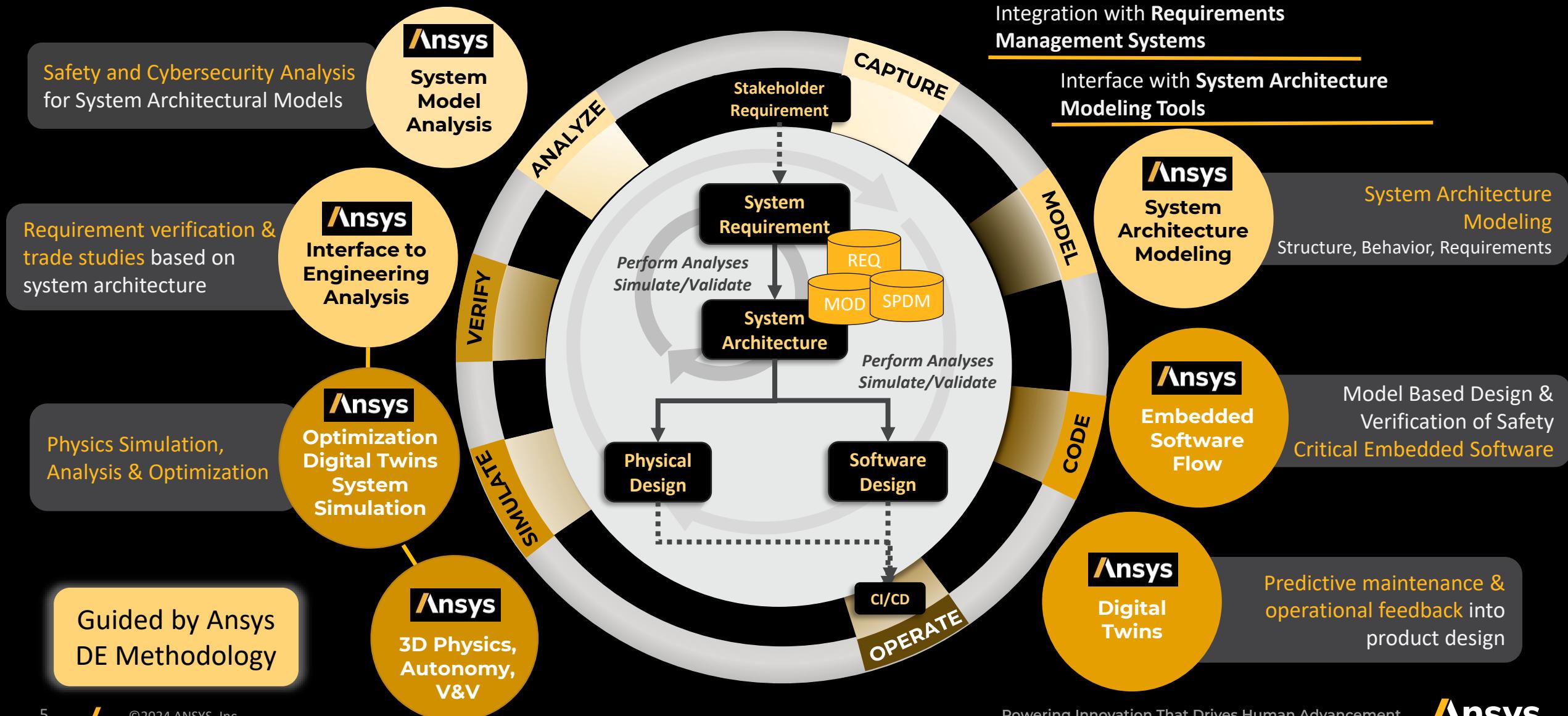


=> We see a broad range of system engineers that need to collaborate in a digital/model-based environment

Shift from Traditional to Digital Systems Engineering

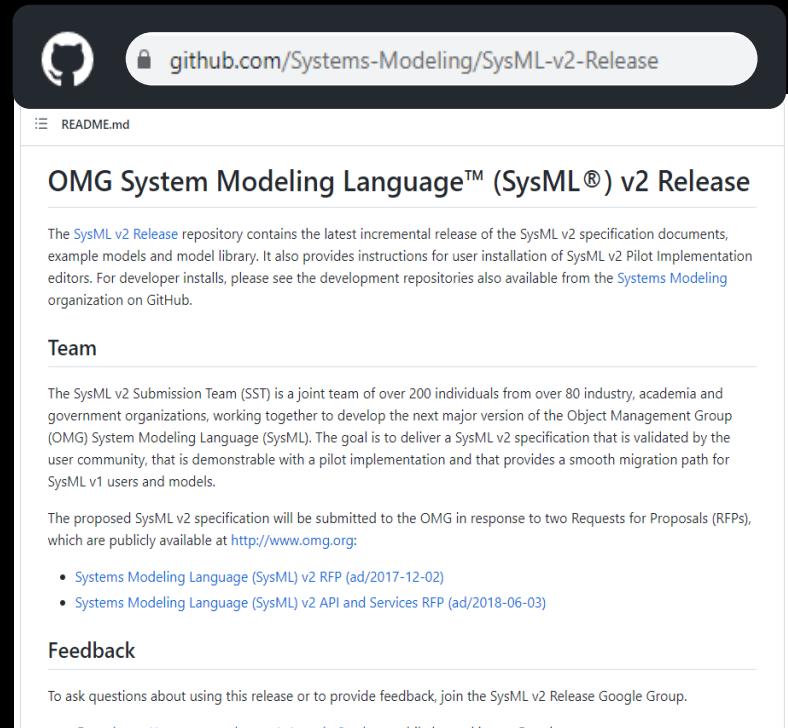


Digital Engineering Capabilities / Supporting MBSE



SysML v2 – Standardization Roadmap

- SysML v2 Finalization ongoing
 - Request For Proposal (RFP) issued to overcome limitations introduced by UML
 - Single specification from the *SysML v2 Submission Team (SST)*
 - Broad MBSE community is contributing and supporting initiative
 - Ansys is part of the SST and OMG FTF leading the group for execution semantics
- Latest SysML v2 specifications available on github



The screenshot shows the GitHub repository for SysML v2. The URL is github.com/Systems-Modeling/SysML-v2-Release. The README.md file is open, displaying the following content:

OMG System Modeling Language™ (SysML®) v2 Release

The [SysML v2 Release](#) repository contains the latest incremental release of the SysML v2 specification documents, example models and model library. It also provides instructions for user installation of SysML v2 Pilot Implementation editors. For developer installs, please see the development repositories also available from the [Systems Modeling](#) organization on GitHub.

Team

The SysML v2 Submission Team (SST) is a joint team of over 200 individuals from over 80 industry, academia and government organizations, working together to develop the next major version of the Object Management Group (OMG) System Modeling Language (SysML). The goal is to deliver a SysML v2 specification that is validated by the user community, that is demonstrable with a pilot implementation and that provides a smooth migration path for SysML v1 users and models.

The proposed SysML v2 specification will be submitted to the OMG in response to two Requests for Proposals (RFPs), which are publicly available at <http://www.omg.org>:

- Systems Modeling Language (SysML) v2 RFP (ad/2017-12-02)
- Systems Modeling Language (SysML) v2 API and Services RFP (ad/2018-06-03)

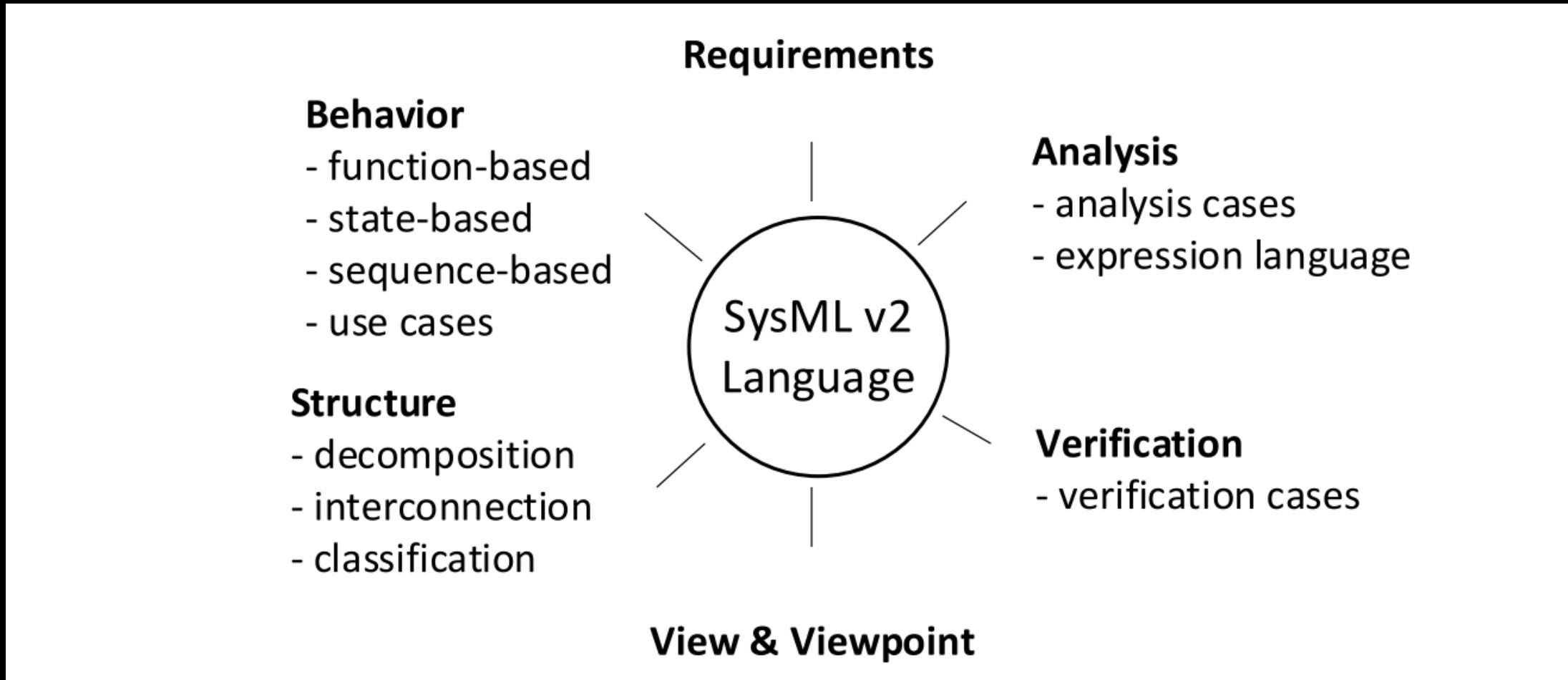
Feedback

To ask questions about using this release or to provide feedback, join the SysML v2 Release Google Group.

• Go to <https://groups.google.com/c/group/sysml-v2-release> while logged into a Google account.



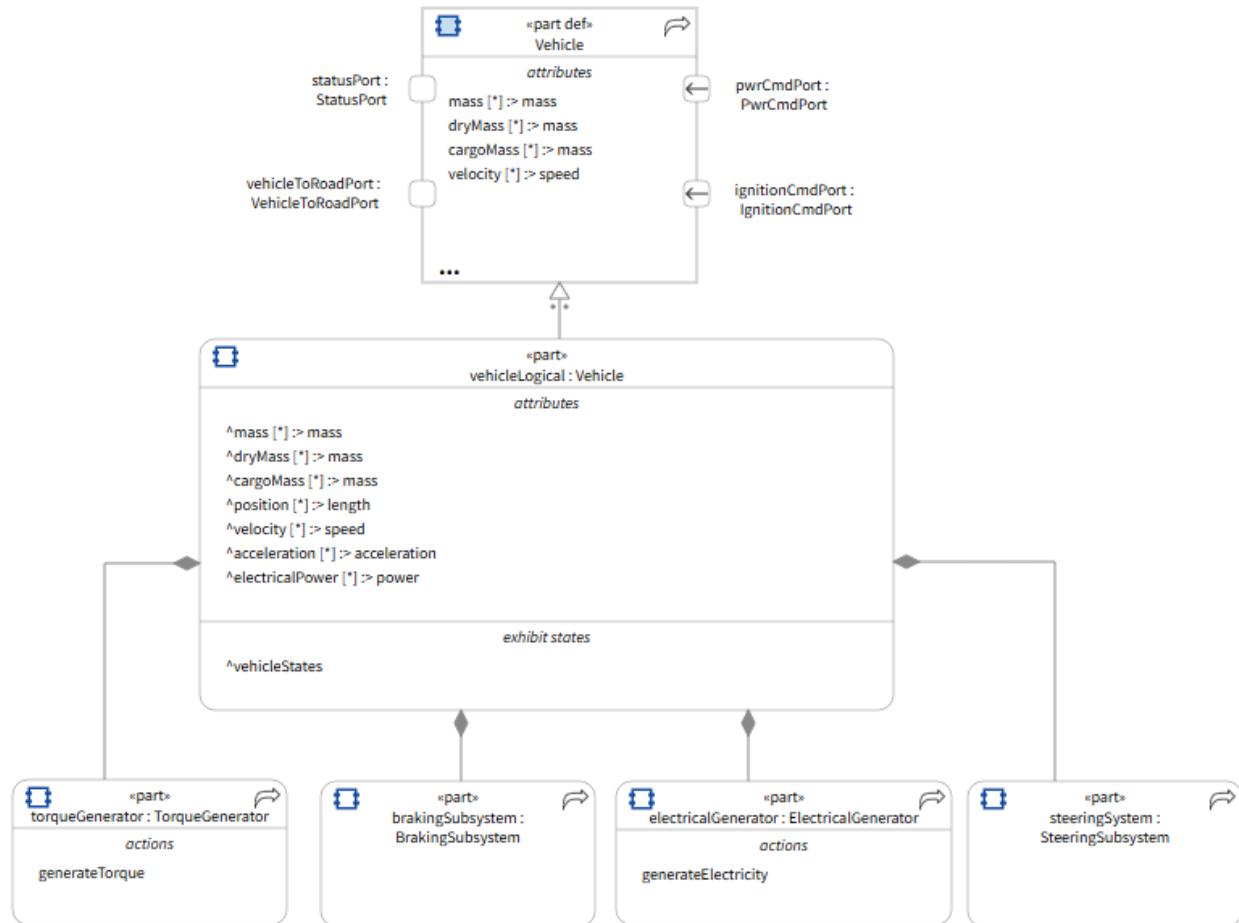
SysML v2: Language Capabilities



SysML v2 Textual and Graphical Syntax

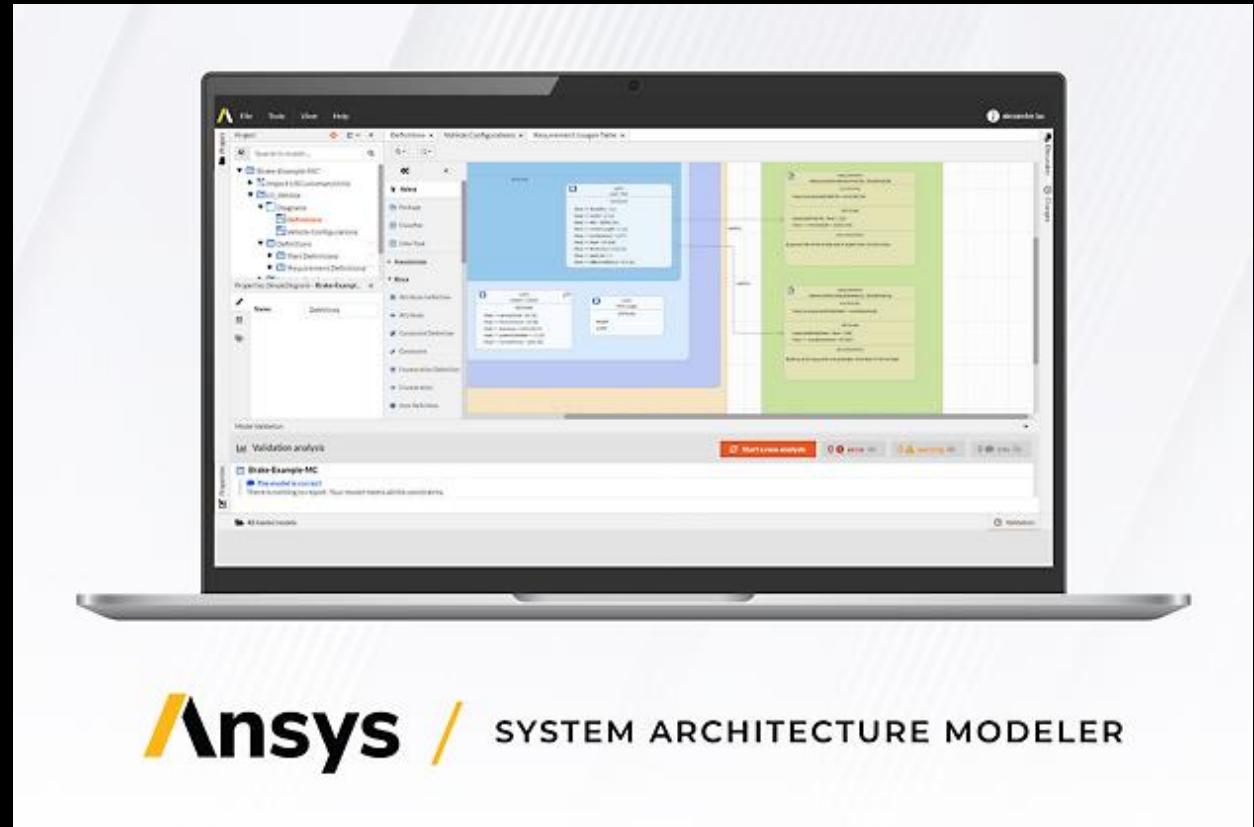
```
20  part def Vehicle {
21      attribute mass :> ISQ::mass;
22      attribute dryMass:>ISQ::mass;
23      attribute cargoMass:>ISQ::mass;
24      attribute position:>ISQ::length;
25      attribute velocity:>ISQ::speed;
26      attribute acceleration:>ISQ::acceleration;
27      attribute electricalPower:>ISQ::power;
28      attribute Tmax:>ISQ::temperature;
29      attribute maintenanceTime: Time::DateTime;
30      attribute brakePedalDepressed: Boolean;
31      port ignitionCmdPort:IgnitionCmdPort;
32      port pwrCmdPort:PwrCmdPort;
33      port vehicleToRoadPort:VehicleToRoadPort;
34      port statusPort:StatusPort;
35      perform action providePower;
36      perform action provideBraking;
37      perform action controlDirection;
38      perform action performSelfTest;
39      perform action applyParkingBrake;
40      perform action senseTemperature;
```

```
486 package VehicleLogicalConfiguration{
487     package PartsTree{
488         #logical part vehicleLogical:Vehicle{
489             part torqueGenerator:TorqueGenerator{
490                 action generateTorque;
491             }
492             part electricalGenerator:ElectricalGenerator{
493                 action generateElectricity;
494             }
495             part steeringSystem:SteeringSubsystem;
496             part brakingSubsystem:BrakingSubsystem;
497         }
498     }
499 }
```



Ansys System Architecture Modeler

- **Cloud-ready SysML v2** modeling solution
- **Collaboration** and ease-of-use are key drivers
- **Connected** to the digital thread



DEMO



Digital Engineering / Ansys Driving Principles

No lock-in

Open solution and built on a solid partner framework - openness in terms of models, data, APIs

Future-proof

Based on **most up-to-date standard SysML V2 & state of the art**

Collaborative

Truly easy-to-use & easy-to-access using **components in web & cloud with enterprise scale real-time collaboration**

Scalable & consistent

Managed source of truth for models and data - holistic approach in terms of **consistent interoperability** with other Enterprise Systems

Connected

Connect to the digital thread and integrate with Ansys multi-physics and system modeling solutions

Summary & Outlook

- SAM implements SysML V2, objectives:
 - Graphical Syntax, textual syntax, and tabular editing
 - 100% complete & standard compliant
 - Seamless integration with digital thread (Ansys and 3rd parties)
- Outlook into 2025
 - Focus on language and API completion (graphical and textual)
 - Increased connectivity to requirements management
 - Improved model and config management (e.g. branches, review support)

! / ?



Thank You for
Listening!

Ansys