



Powering Innovation That Drives Human Advancement

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# **Real-time collaborative system modelling with Ansys System Architecture Modeler (SAM)**

Michael Soden / Senior Product Manager

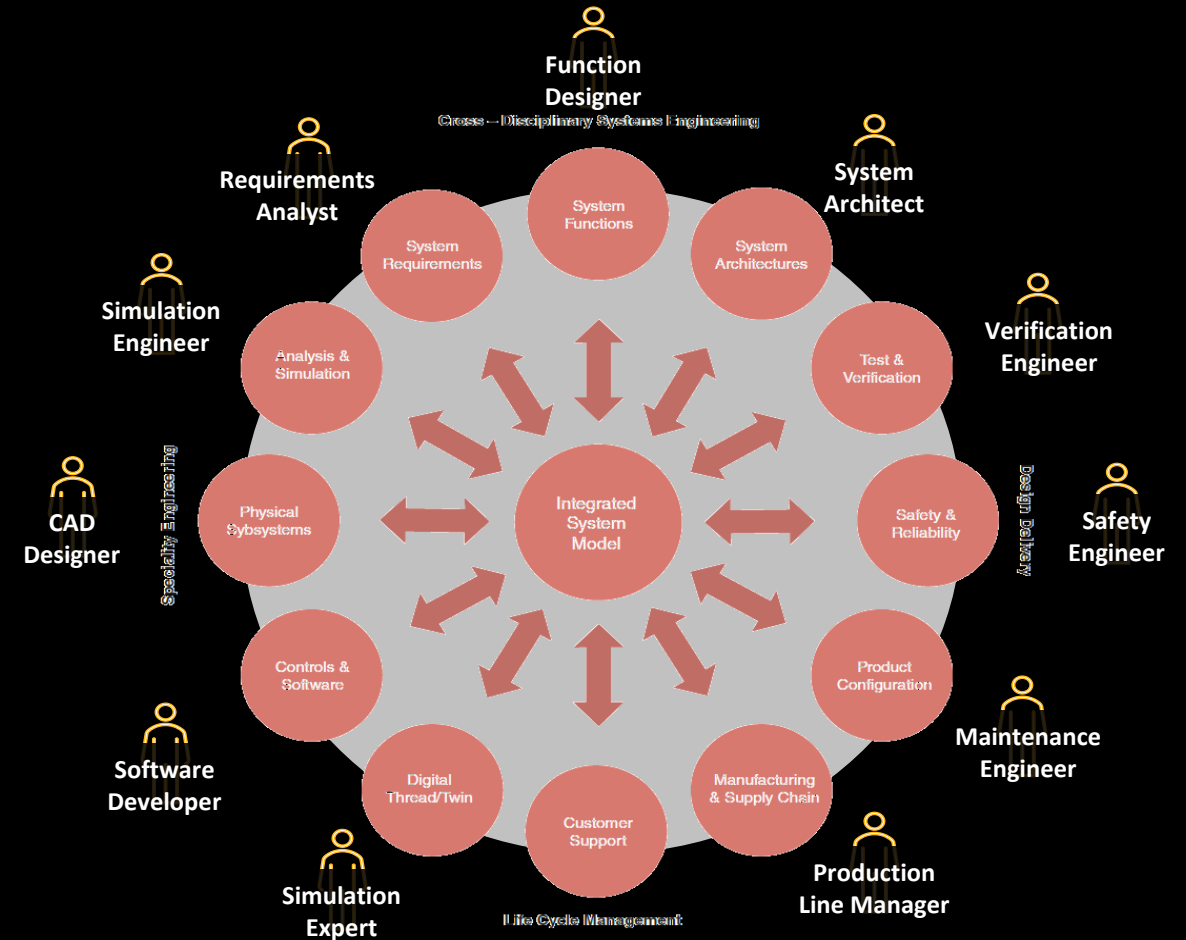
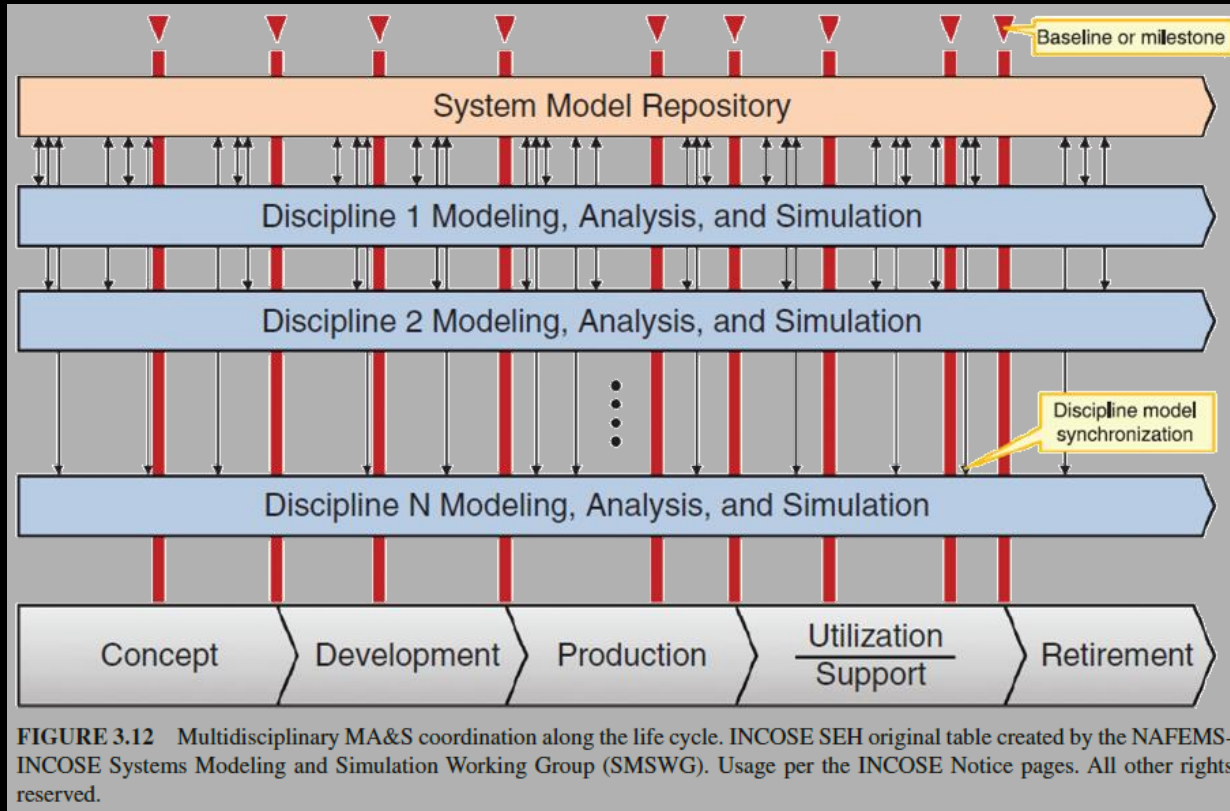
2024-11-23

# Agenda

- MBSE and Digital Engineering at Ansys
- System Architecture Modeler (SAM)
- Q&A

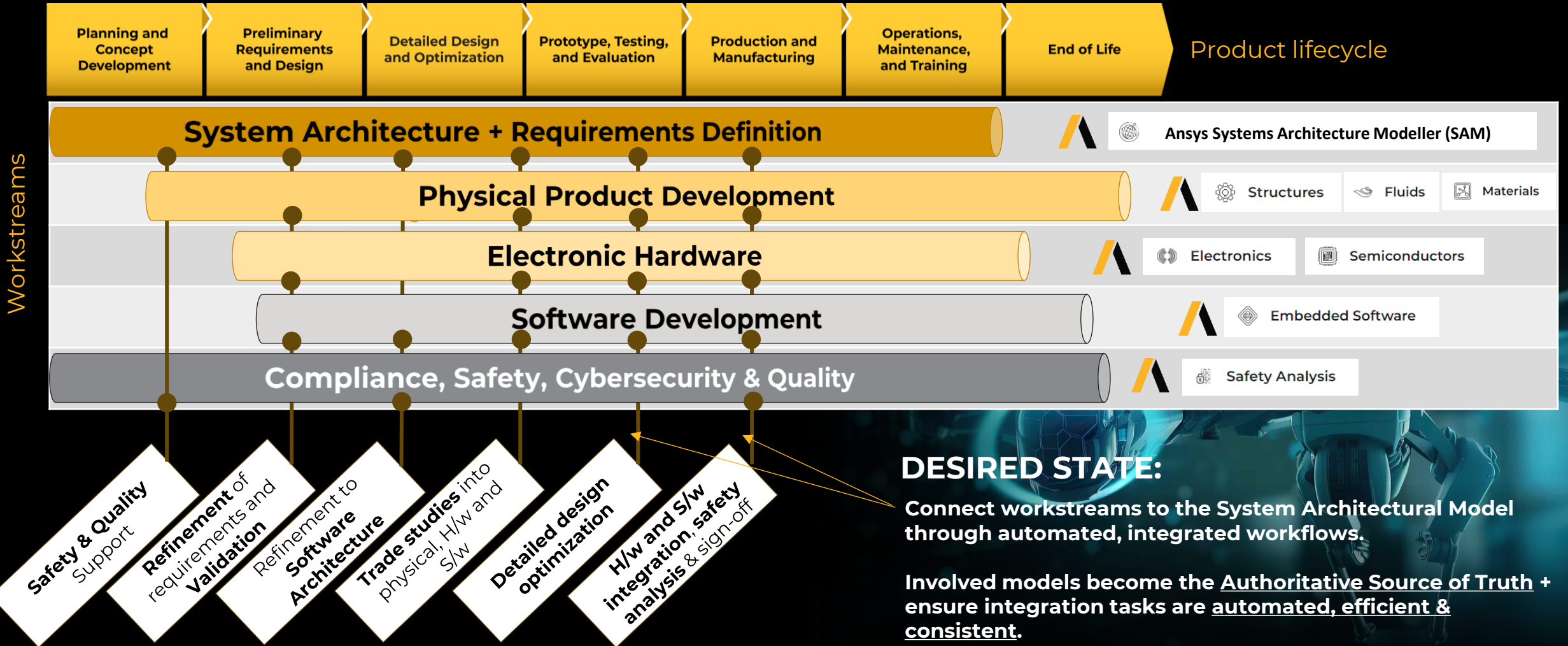


# Challenge: Model-based Integration across multiple Disciplines

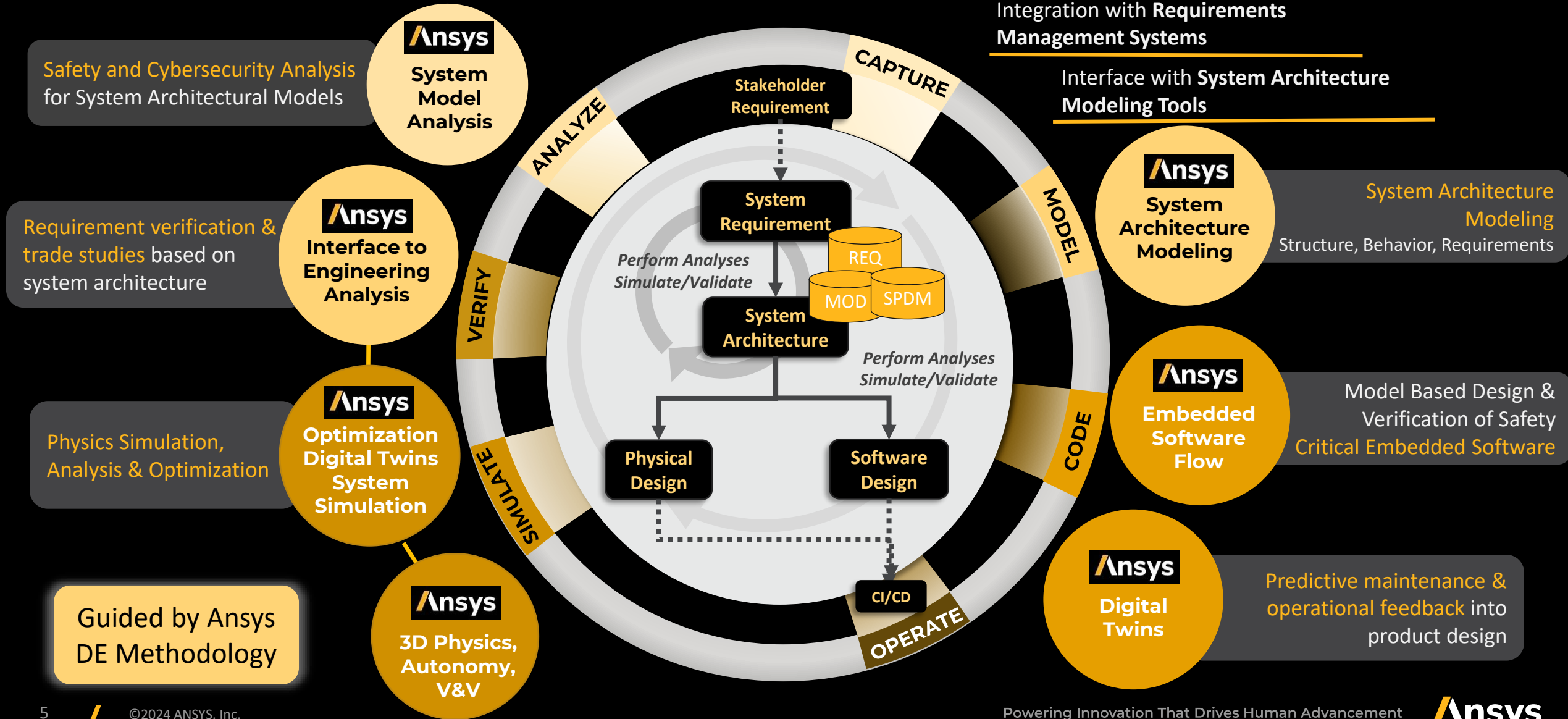


=> We see a broad range of system engineers that need to collaborate in a digital/model-based environment

# Shift from Traditional to Digital Systems Engineering



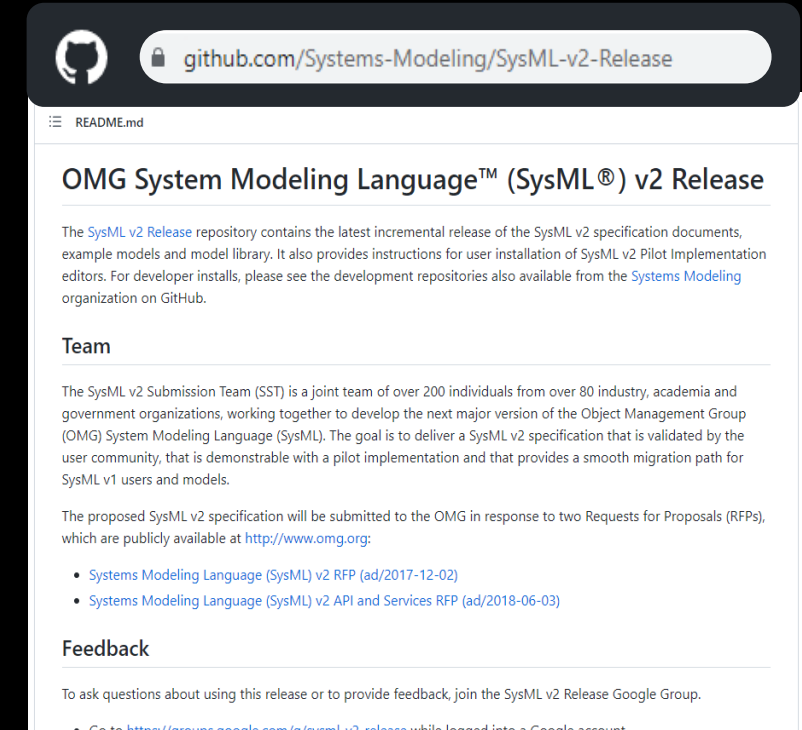
# Digital Engineering Capabilities / Supporting MBSE



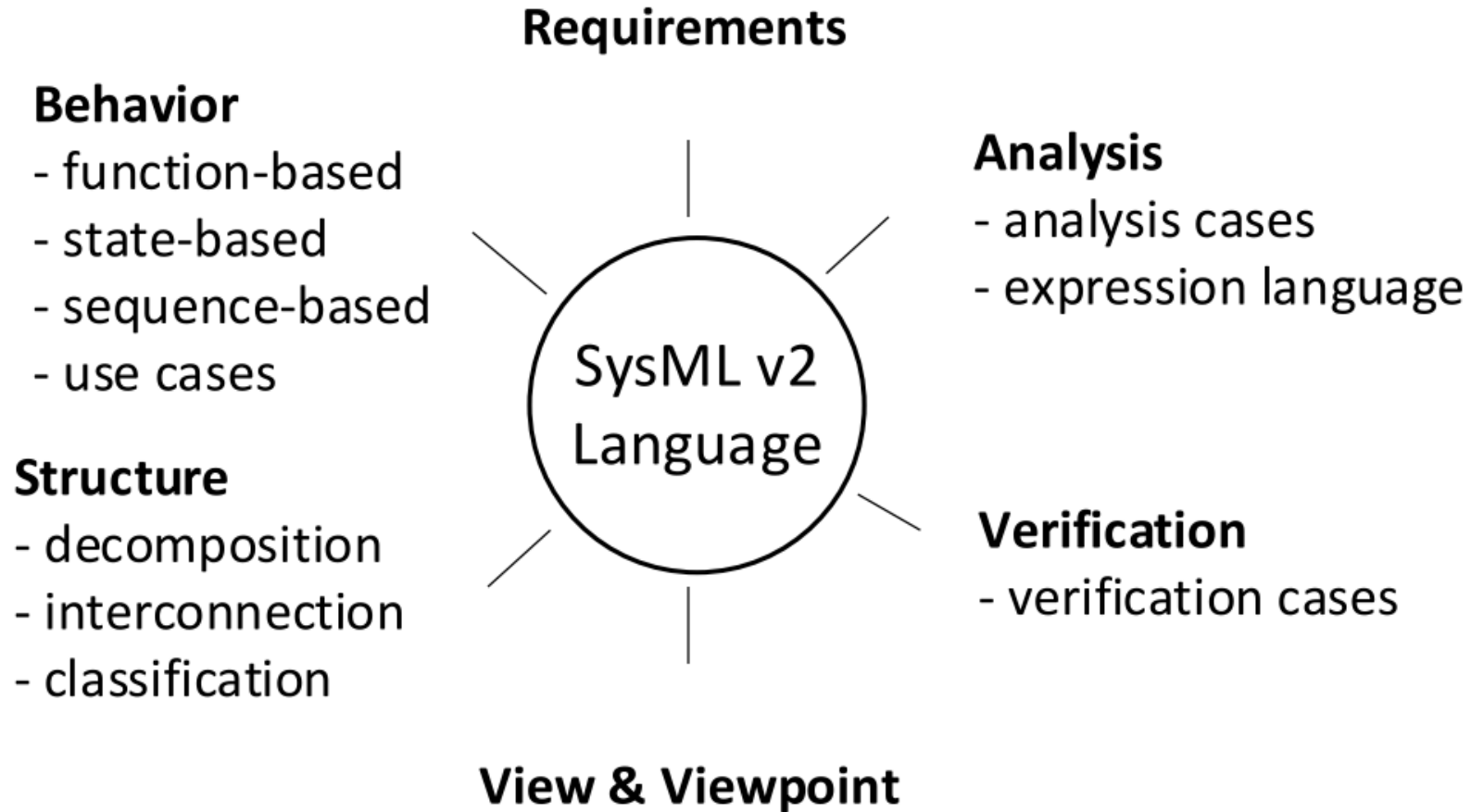


# SysML v2 – Standardization Roadmap

- SysML v2 Finalization ongoing
  - Request For Proposal (RFP) issued to overcome limitations introduced by UML
  - Single specification from the *SysML v2 Submission Team* (SST)
  - Broad MBSE community is contributing and supporting initiative
  - Ansys is part of the SST and OMG FTF leading the group for execution semantics
- Latest SysML v2 specifications available on github



# SysML v2: Language Capabilities



Source: Introduction to the SysML v2 Language (SST Presentations)

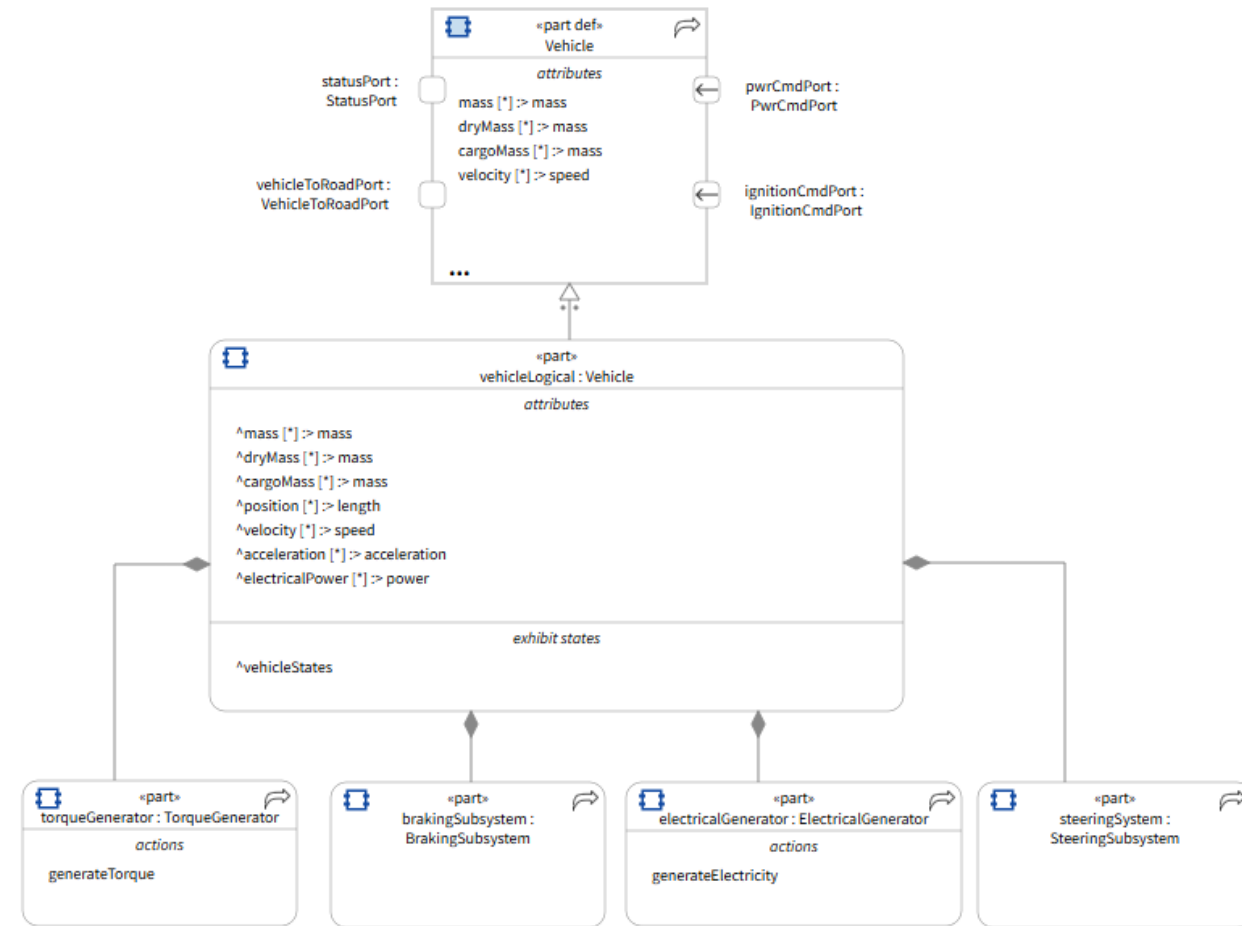
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13 Dec 2021

# SysML v2 Textual and Graphical Syntax

```
20 part def Vehicle {
21   attribute mass :> ISQ::mass;
22   attribute dryMass :> ISQ::mass;
23   attribute cargoMass :> ISQ::mass;
24   attribute position :> ISQ::length;
25   attribute velocity :> ISQ::speed;
26   attribute acceleration :> ISQ::acceleration;
27   attribute electricalPower :> ISQ::power;
28   attribute Tmax :> ISQ::temperature;
29   attribute maintenanceTime : Time::DateTime;
30   attribute brakePedalDepressed : Boolean;
31   port ignitionCmdPort : IgnitionCmdPort;
32   port pwrCmdPort : PwrCmdPort;
33   port vehicleToRoadPort : VehicleToRoadPort;
34   port statusPort : StatusPort;
35   perform action providePower;
36   perform action provideBraking;
37   perform action controlDirection;
38   perform action performSelfTest;
39   perform action applyParkingBrake;
40   perform action senseTemperature;
```

```
486 package VehicleLogicalConfiguration{
487   package PartsTree{
488     #logical part vehicleLogical:Vehicle{
489       part torqueGenerator:TorqueGenerator{
490         action generateTorque;
491       }
492       part electricalGenerator:ElectricalGenerator{
493         action generateElectricity;
494       }
495       part steeringSystem:SteeringSubsystem;
496       part brakingSubsystem:BrakingSubsystem;
497     }
498   }
499 }
```

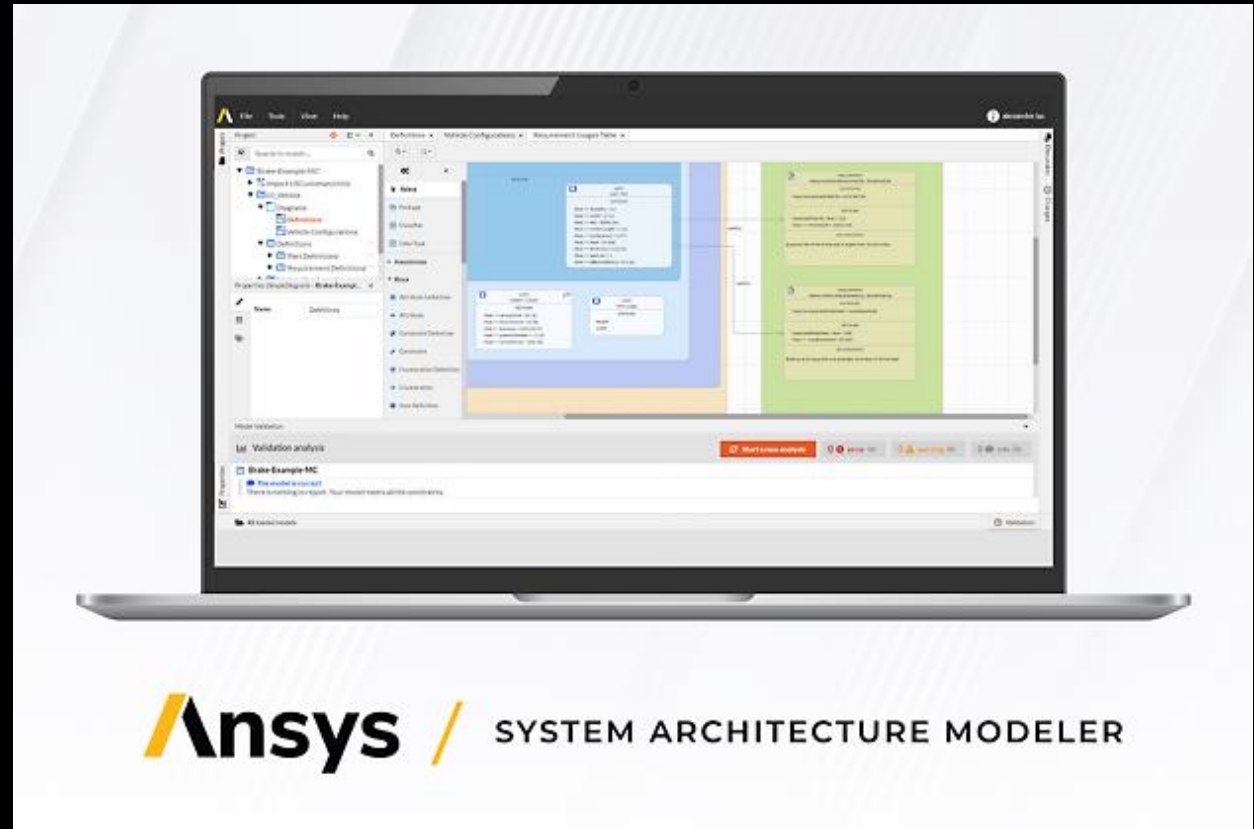




# Digital Engineering / SAM

## Ansys System Architecture Modeler

- **Cloud-ready SysML v2** modeling solution
- **Collaboration** and ease-of-use are key drivers
- **Connected** to the digital thread



# DEMO



# Digital Engineering / Ansys Driving Principles

## No lock-in

**Open solution** and built on a solid partner framework - openness in terms of models, data, APIs

## Future-proof

Based on **most up-to-date standard SysML V2 & state of the art**

## Collaborative

Truly easy-to-use & easy-to-access using **components in web & cloud with enterprise scale real-time collaboration**

## Scalable & consistent

Managed source of truth for models and data - holistic approach in terms of **consistent interoperability** with other Enterprise Systems

## Connected

**Connect to the digital thread** and integrate with Ansys multi-physics and system modeling solutions

# Summary & Outlook

- SAM implements SysML V2, objectives:
  - Graphical Syntax, textual syntax, and tabular editing
  - 100% complete & standard compliant
  - Seamless integration with digital thread (Ansys and 3<sup>rd</sup> parties)
- Outlook into 2025
  - Focus on language and API completion (graphical and textual)
  - Increased connectivity to requirements management
  - Improved model and config management (e.g. branches, review support)

! / ?

Thank You for  
Listening!

The Ansys logo is displayed on a black background. It features a stylized 'A' icon composed of two parallel diagonal lines, one yellow and one white, followed by the word 'Ansys' in a white, sans-serif font.